**Pb Free Product** 

#### NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE0157AK uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

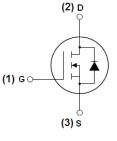
- $V_{DS}$  = 100V, $I_{D}$  =57A  $R_{DS(ON)}$  < 16mΩ @  $V_{GS}$ =10V (Typ:12mΩ)
- Special process technology for high ESD capability
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E<sub>AS</sub>
- Excellent package for good heat dissipation

#### **Application**

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



#### Schematic diagram



Marking and pin assignment



TO-252-2L top view

#### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE0157AK	NCE0157AK	TO-252-2L	Ø330mm	12mm	2500 units

#### Absolute Maximum Ratings (T<sub>c</sub>=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	57	Α
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	40	А
Pulsed Drain Current	I <sub>DM</sub>	190	А
Maximum Power Dissipation	P <sub>D</sub>	170	W
Derating factor		1.13	W/°C
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	342	mJ
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 175	$^{\circ}$ C



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# NCE0157AK

## **Thermal Characteristic**

Thermal Resistance,Junction-to-Case <sup>(Note 2)</sup>	$R_{ heta JC}$	0.88	°C/W	]
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Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	100	110	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =100V,V <sub>GS</sub> =0V	OV,V <sub>GS</sub> =0V -		1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	<u> </u>		•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	0.8	1.2	1.8	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	12	16	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =25V,I <sub>D</sub> =20A	32	-	-	S
Dynamic Characteristics (Note4)	<u> </u>		•			
Input Capacitance	C <sub>lss</sub>	\/ F0\/\\ 0\/	-	4118	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =50V, $V_{GS}$ =0V,	-	210	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	169	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	12	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =50V, $I_D$ =20A	-	55	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{GEN}$ =2.5 $\Omega$	-	45	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	47	-	nS
Total Gate Charge	Qg	V 50V/1 00A	-	111	-	nC
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> =50V,I <sub>D</sub> =20A,	-	11.5	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	24	-	nC
Drain-Source Diode Characteristics	<u> </u>		•			
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =20A	-	0.85	1.2	V
Diode Forward Current (Note 2)	Is		-	-	57	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF = 20A	-	36	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	56	-	nC
Forward Turn-On Time	t <sub>on</sub>	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD				

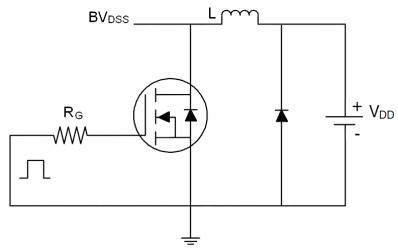
#### Notes:

- **1.** Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25 $^{\circ}$ C,V<sub>DD</sub>=50V,V<sub>G</sub>=10V,L=0.5mH,Rg=25 $\Omega$

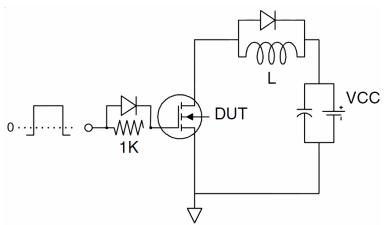


#### **Test Circuit**

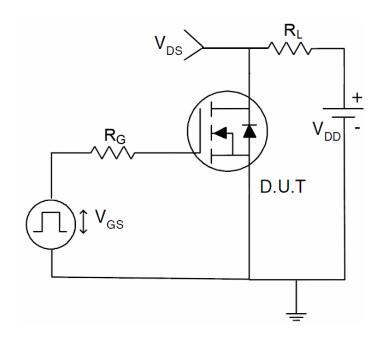
## 1) E<sub>AS</sub> test Circuit



## 2) Gate charge test Circuit

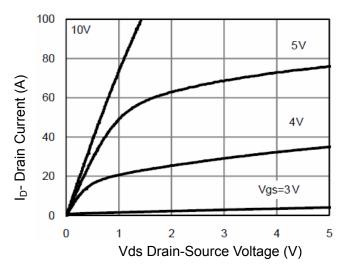


#### 3) Switch Time Test Circuit

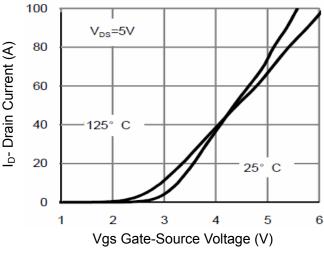




### **Typical Electrical and Thermal Characteristics (Curves)**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

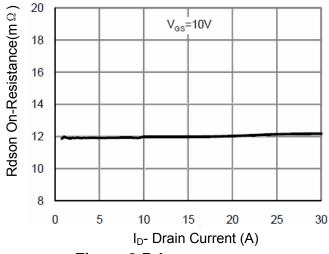


Figure 3 Rdson- Drain Current

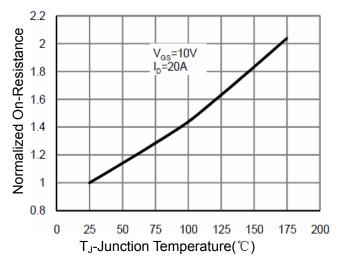


Figure 4 Rdson-JunctionTemperature

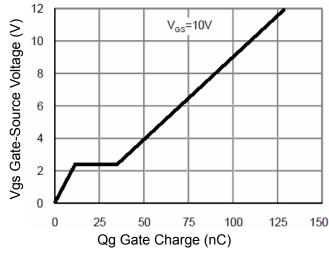


Figure 5 Gate Charge

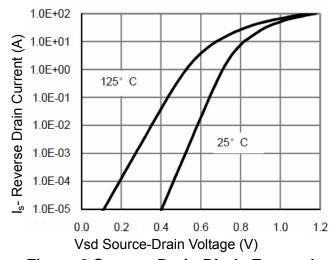


Figure 6 Source- Drain Diode Forward



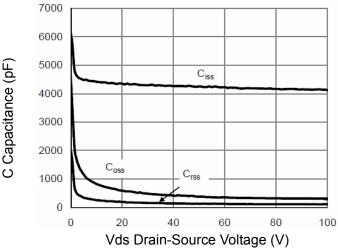


Figure 7 Capacitance vs Vds

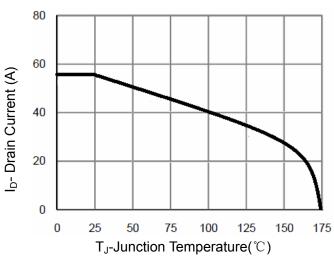


Figure 9 I<sub>D</sub> Current De-rating

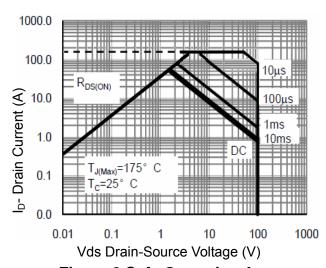


Figure 8 Safe Operation Area

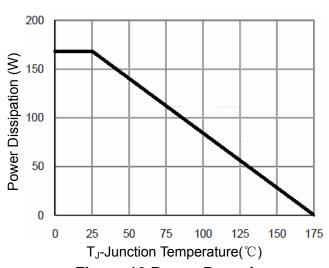


Figure 10 Power De-rating

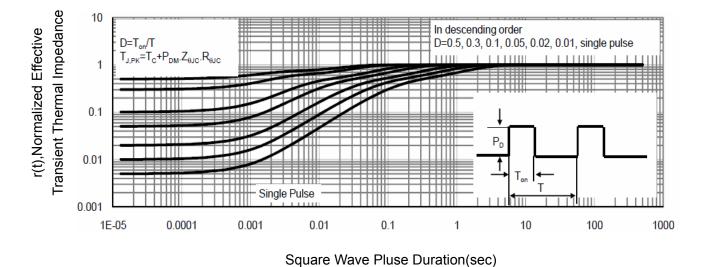
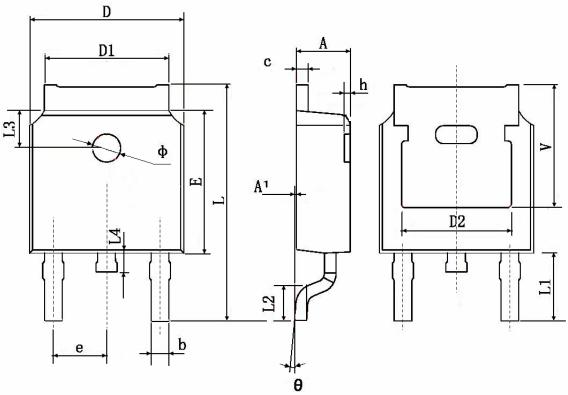


Figure 11 Normalized Maximum Transient Thermal Impedance



## **TO-252 Package Information**



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.830 TYP.		0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900	00 TYP. 0.114		TYP.	
L2	1.400	1.700	0.055	0.067	
L3	1.600	0 TYP. 0.063 TYP.		TYP.	
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		



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# NCE0157AK

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