

NCE P-Channel Enhancement Mode Power MOSFET

Description

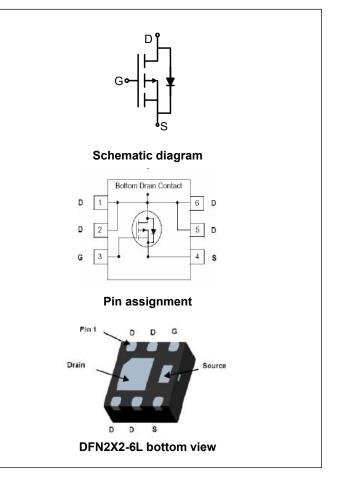
The NCE1216 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages .This device is suitable for use as a load switching application and a wide variety of other applications.

General Features

- $V_{DS} = -12V, I_D = -16A$ $R_{DS(ON)} < 22m\Omega @ V_{GS} = -2.5V$ $R_{DS(ON)} < 18m\Omega @ V_{GS} = -4.5V$
- Advanced trench MOSFET process technology
- Ultra low on-resistance with low gate charge

Application

- PWM applications
- Load switch
- Battery charge in cellular handset



Package marking and ordering information

V	<u> </u>				
Device Marking	Device	Device Package	Reel Size	Tape Width	Quantity
1216	NCE1216	DFN2X2-6L	-	-	-

Absolute maximum ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	-12	V
Gate-Source Voltage	Vgs	±12	V
Drain Current-Continuous	Ι _D	-16	A
Drain Current -Pulsed (Note 1)	I _{DM}	-65	A
Maximum Power Dissipation (T_c=25 $^{\circ}$ C)	P _D (T _C =25℃)	18	W
Maximum Power Dissipation (T_A=25 $^{\circ}$ C)	P _D (T _A =25℃)	2.5	W
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	Rejc	6.9	°C /W
Thermal Resistance, Junction-to-Ambient (Note 2)	Reja	50	°C/W



Electrical characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics	· · ·					
Drain-Source Breakdown Voltage	V _(BR) DSS	V _{GS} =0V I _D =-250µA	-12	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-12V,V _{GS} =0V	-	-	-1	μA
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±12V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	I		-			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-0.4	-0.7	-1	V
Durin Original On Otata Desistance	_	V _{GS} =-4.5V, I _D =-6.7A	-	11.5	18	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-2.5V, I _D =-6.2A	-	14	22	mΩ
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-6.7A	20	-	-	S
Dynamic Characteristics (Note4)						1
Input Capacitance	Clss	<u>)</u> ////////////////////////////////////	-	2700	-	PF
Output Capacitance	Coss	V_{DS} =-10V, V_{GS} =0V,	-	680	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	590	-	PF
Switching Characteristics (Note 4)						1
Turn-on Delay Time	t _{d(on)}		-	11	-	nS
Turn-on Rise Time	tr	V _{DD} =-10V,I _D =-1A	-	35	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-4.5V, R_{GEN} =10 Ω	-	30	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg		-	35	48	nC
Gate-Source Charge	Q _{gs}	V _{DS} =-6V,I _D =-10A,	-	5	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =-4.5V	-	10	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-8A	-	-	-1.2	V
Diode Forward Current (Note 2)	Is		_	-	-16	А

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production



off

10%

90%

50%

90%

t_{d(off)}

INVERTED

PULSE WIDTH

Figure 2:Switching Waveforms

t

t_{on}

t

10%

50%

90%

t_{d(on)}

V_{OUT}

V_{IN}

10%

Typical Electrical and Thermal Characteristics

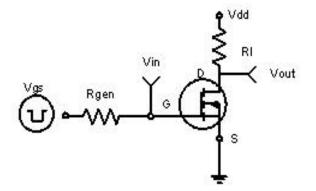
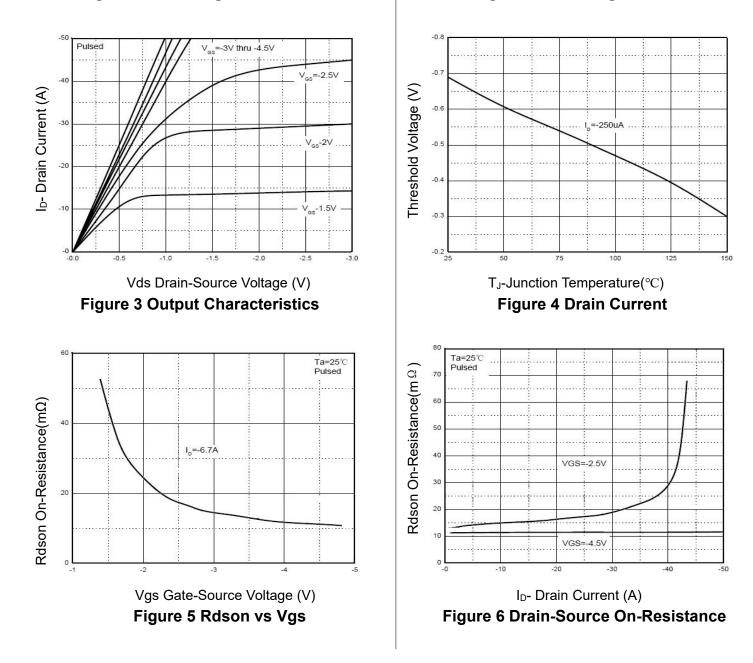


Figure 1:Switching Test Circuit





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NCE1216

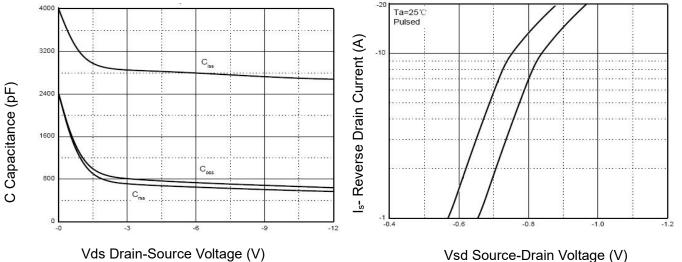
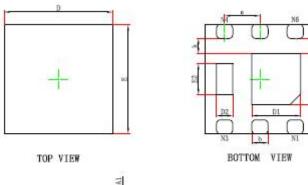


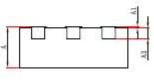
Figure 7 Capacitance vs Vds

Vsd Source-Drain Voltage (V) Figure 8 Source- Drain Diode Forward



DFN2X2-6L Package Information





SI	DE	VI	EW
	84.84		6e 11

Symbol	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	0.700	0.800	0.028	0.031	
A1	0.000	0.050	0.000	0.002	
A3	0.203	REF.	0.008	REF.	
D	1.924	2.076	0.076	0.082	
E	1.924	2.076	0.076	0.082	
D1	0.800	1.000	0.031	0.039	
E1	0.850	1.050	0.033	0.041	
D2	0.200	0.400	0.008	0.016	
E2	0.460	0.660	0.018	0.026	
k	0.200MIN.		0.008MIN.		
b	0.250	0.350	0.010	0.014	
е	0.650TYP.		0.026TYP.		
L	0.174	0.326	0.007	0.013	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ± 0.10 mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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