

## NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

The NCE60ND09AS uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

V<sub>DS</sub> = 60V,I<sub>D</sub> =9A

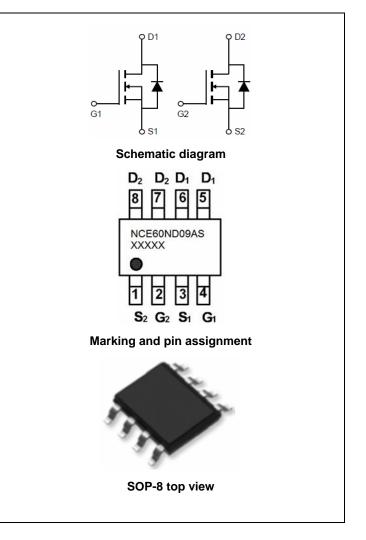
 $R_{DS(ON)} < 15m\Omega$  @  $V_{GS}=10V$  (Typ:10m $\Omega$ )

 $R_{DS(ON)} < 18m\Omega @ V_{GS}=4.5V$  (Typ:14m $\Omega$ )

- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Low gate to drain charge to reduce switching losses

#### **Application**

- Power switching application
- Load switch



# **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE60ND09AS	NCE60ND09AS	SOP-8	-	-	-

### Absolute Maximum Ratings (T<sub>C</sub>=25 ℃unless otherwise noted)

<b>5</b> \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \			
Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current-Continuous	I <sub>D</sub>	9	Α
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	6.4	Α
Pulsed Drain Current	I <sub>DM</sub>	36	Α
Maximum Power Dissipation	P <sub>D</sub>	2.6	W
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}$

### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	48	°C/W
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# NCE60ND09AS

Electrical Characteristics (TC=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	1.2	1.8	2.2	V
Drain Course On Ctate Desistance		V <sub>GS</sub> =10V, I <sub>D</sub> =9A	-	10	15	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =9A	60	18	mΩ	
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =9A	25	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ -20\/\/ -0\/	-	2180	-	PF
Output Capacitance	Coss	$V_{DS}$ =30V, $V_{GS}$ =0V,	-	350	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	270	-	PF
Switching Characteristics (Note 4)			•			•
Turn-on Delay Time	t <sub>d(on)</sub>		-	8.5	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =30V, $R_L$ =1 $\Omega$	-	6	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{GEN}$ =3 $\Omega$	-	30	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	5	-	nS
Total Gate Charge	Qg	\/ 00\/ L 0A	-	58	-	nC
Gate-Source Charge	Q <sub>gs</sub>	, ,	-	8	-	nC
Gate-Drain Charge	$Q_{gd}$	- V <sub>DS</sub> =30V,I <sub>D</sub> =9A,		17	-	nC
Drain-Source Diode Characteristics	,		•			
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =9A	-	-	1.2	V
Diode Forward Current (Note 2)	Is	-	-	-	9	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF=9A	-	30	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	44	-	nC
				1		

#### Notes:

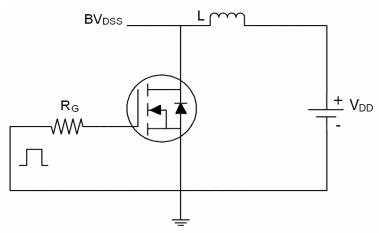
- $\textbf{1.} \ \textbf{Repetitive Rating: Pulse width limited by maximum junction temperature.}$
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- **3.** Pulse Test: Pulse Width  $\leq 300 \mu$ s, Duty Cycle  $\leq 2\%$ .
- 4. Guaranteed by design, not subject to production
- **5.** EAS condition: Tj=25  $^{\circ}\text{C}$  ,VDD=50V,VG=10V,L=0.5mH,Rg=25 $\Omega$

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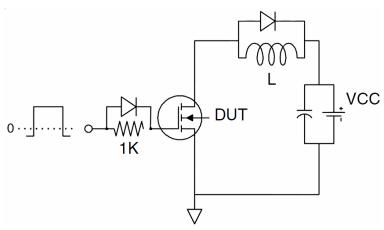


# **Test Circuit**

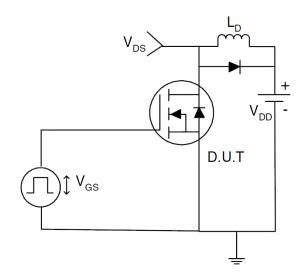
# 1) E<sub>AS</sub> test Circuit



# 2) Gate charge test Circuit

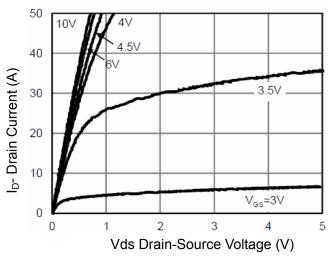


# 3) Switch Time Test Circuit

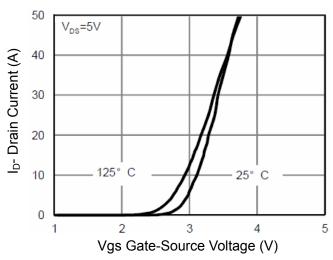




# Typical Electrical and Thermal Characteristics (Curves)



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

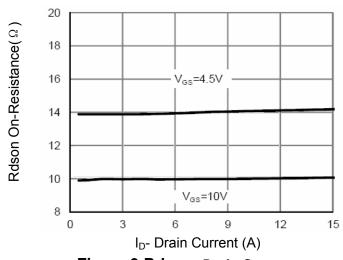


Figure 3 Rdson- Drain Current

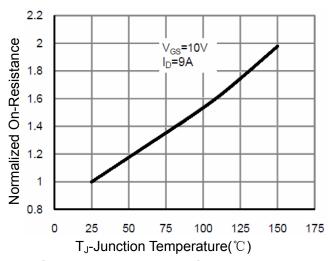


Figure 4 Rdson-JunctionTemperature

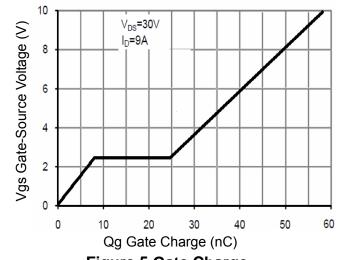


Figure 5 Gate Charge

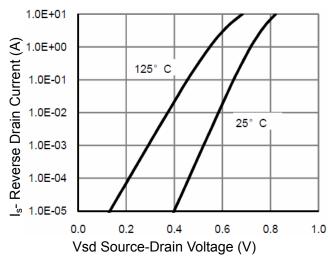
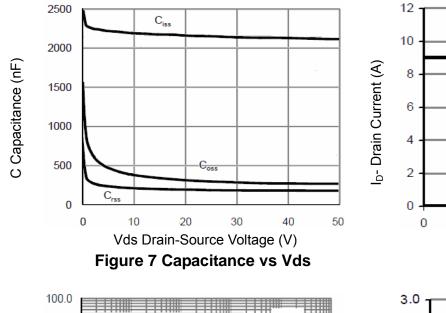


Figure 6 Source- Drain Diode Forward





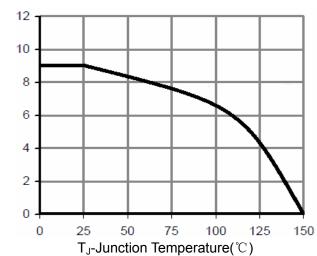
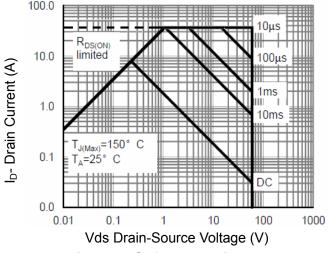
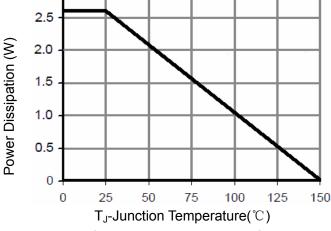


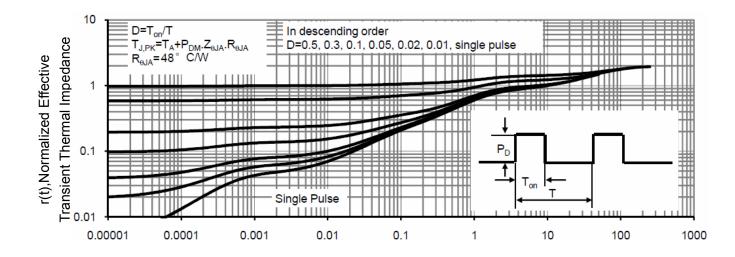
Figure 9 Current De-rating





**Figure 8 Safe Operation Area** 

Figure 10 Power De-rating

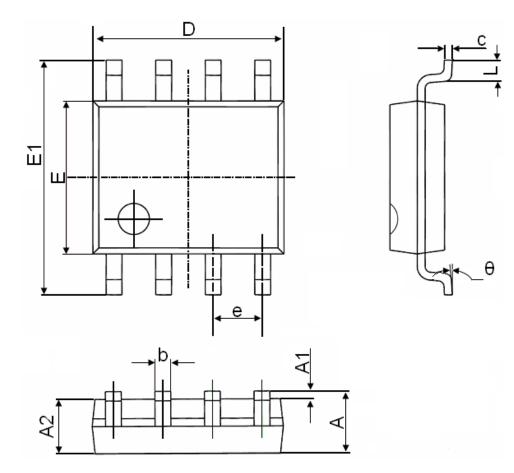


**Figure 11 Normalized Maximum Transient Thermal Impedance** 

Square Wave Pluse Duration(sec)



# **SOP-8 Package Information**



Combal	Dimensions I	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270	(BSC)	0.050(E	BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	

#### http://www.ncepower.com

# NCE60ND09AS

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