

NCE N-Channel Enhancement Mode Power MOSFET



The NCE80H12D uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

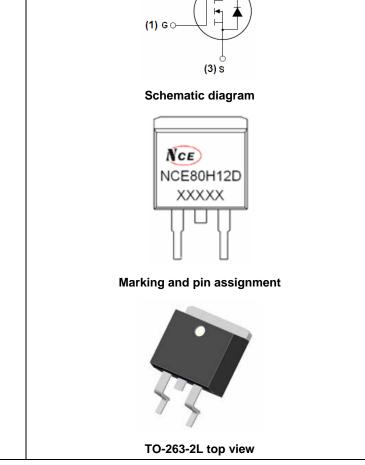
- V_{DS} =80V,I_D =120A
 R_{DS(ON)} <6mΩ @ V_{GS}=10V
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Automotive applications
- Hard switched and high frequency circuits
- Uninterruptible power supply

100% UIS TESTED!

100% ΔVds TESTED!



(2) D

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE80H12D	NCE80H12D	TO-263-2L	-	-	-

Absolute Maximum Ratings (T_c=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	80	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	Ι _D	120	А
Drain Current-Continuous(T _C =100 ℃)	I _D (100℃)	84	А
Pulsed Drain Current	I _{DM}	450	А
Maximum Power Dissipation	PD	220	W
Derating factor		1.47	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	1400	mJ
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 175	°C



Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	R _{θJC}	0.68	°C/W
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Electrical Characteristics (T_C=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	80	89	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =80V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =40A	-	4.9	6	mΩ
Forward Transconductance	g fs	V _{DS} =25V,I _D =57A	90	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C _{lss}		-	6500	-	PF
Output Capacitance	C _{oss}	V _{DS} =25V,V _{GS} =0V, F=1.0MHz	-	520	-	PF
Reverse Transfer Capacitance	C _{rss}		-	460	-	PF
Switching Characteristics (Note 4)	·					
Turn-on Delay Time	t _{d(on)}		-	26	-	nS
Turn-on Rise Time	tr	V_{DD} =30V,I _D =2A,R _L =15 Ω	-	24	-	nS
Turn-Off Delay Time	t _{d(off)}	V _{GS} =10V,R _G =2.5Ω	-	91	-	nS
Turn-Off Fall Time	t _f		-	39	-	nS
Total Gate Charge	Qg)/ _20)// _20)	-	163		nC
Gate-Source Charge	Q _{gs}	V _{DS} =30V,I _D =30A, V _{GS} =10V	-	31		nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	64		nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =40A	-		1.2	V
Diode Forward Current (Note 2)	I _S		-	-	120	А
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF = 40A	-	42	60	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	66	80	nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)			y LS+LD)	

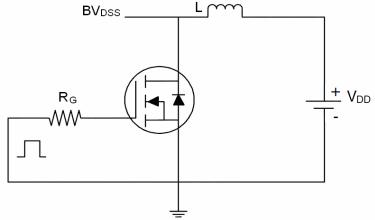
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition: Tj=25 $^\circ \!\! \mathbb{C}$,V_{DD}=40V,V_G=10V,L=0.5mH,Rg=25\Omega

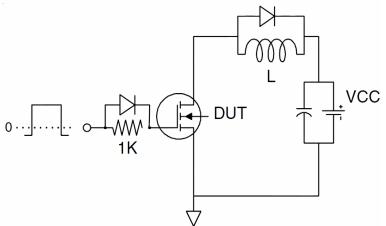


Test circuit

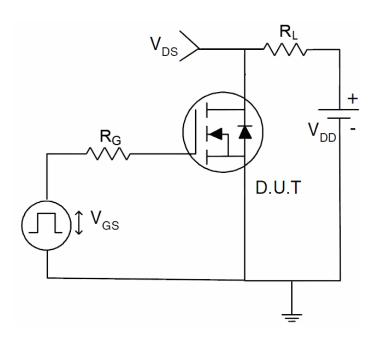
1) E_{AS} test Circuit



2) Gate charge test Circuit

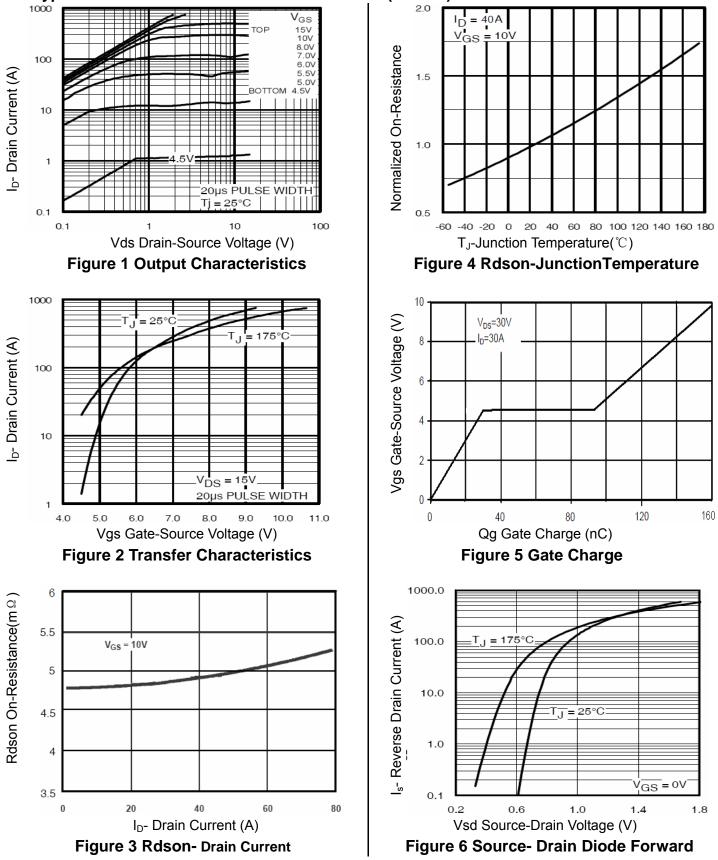


3) Switch Time Test Circuit









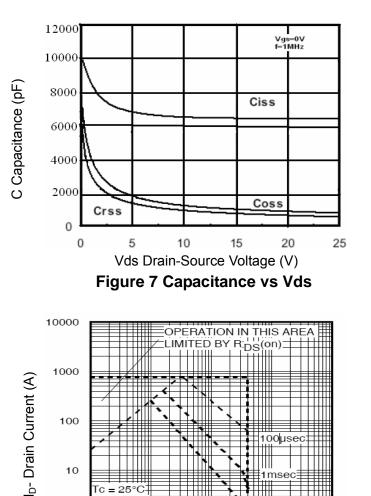


10

1

0

Tc = 25°C Tj = 175°C Single Pulse



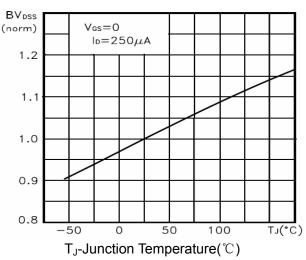


Figure 9 **BV_{DSS} vs Junction Temperature**

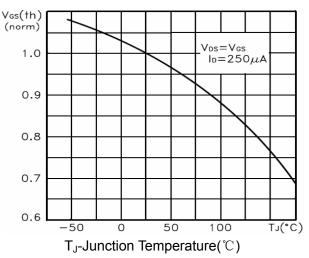
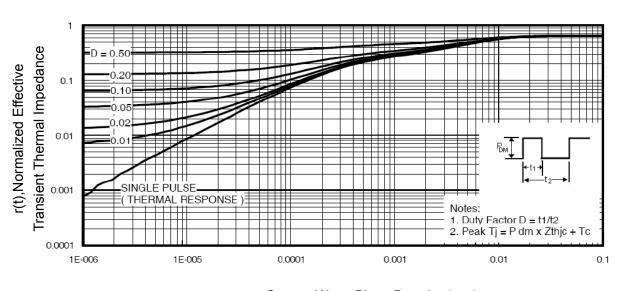


Figure 10 V_{GS(th)} vs Junction Temperature



10msec

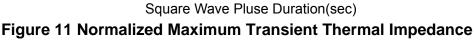
1000

100

10

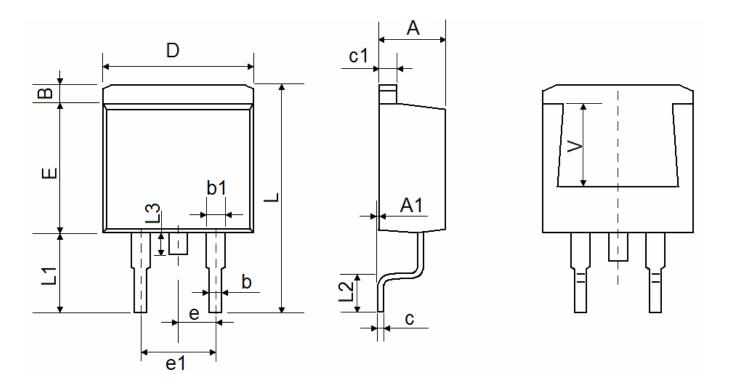
Vds Drain-Source Voltage (V)

Figure 8 Safe Operation Area





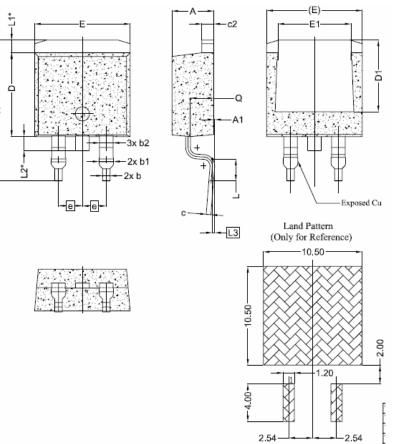
TO-263-2L Package Information (-)



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	4.470	4.670	0.176	0.184	
A1	0.000	0.150	0.000	0.006	
В	1.170	1.370	0.046	0.054	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
С	0.310	0.530	0.012	0.021	
c1	1.170	1.370	0.046	0.054	
D	10.010	10.310	0.394	0.406	
E	8.500	8.900	0.335	0.350	
е	2.540) TYP.	0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
L	15.050	15.450	0.593	0.608	
L1	5.080	5.480	0.200	0.216	
L2	2.340	2.740	0.092	0.108	
L3	1.300	1.700	0.051	0.067	
V	5.60	0 REF	0.220	REF	



TO-263-2L Package Information (ニ)



SYMBOL	DIMENSIONS			
	MIN.	NOM.	MAX.	
А	4.24	4.44	4.64	
A1	0.00	0.10	0.25	
ь	0.70	0.80	0.90	
b1	1.20	1.55	1.75	
b2	1,20	1,45	1,70	
с	c 0.40		0.60	
c2	1,15	1,27	1,40	
D	8.82	8.92	9.02	
D1	6.86	7.65	_	
E	9.96	10.16	10.36	
E1	6.89 7.77		7.89	
е	2.54 BSC			
н	14,61 15,00		15,88	
L	1.78 2.32		2.79	
L1	1.36 REF.			
L2	1.50 REF.			
L3	0.25 BSC			
Q	Q 2.30		2.70	



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