

NCE N-Channel Super Trench Power MOSFET

Description

The NCEP1570GU uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

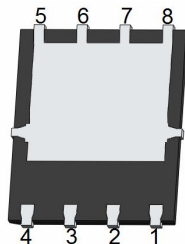
- $V_{DS} = 150V, I_D = 70A$
 $R_{DS(ON)} = 13.5m\Omega$ (typical) @ $V_{GS} = 10V$
- Excellent gate charge x $R_{DS(on)}$ product (FOM)
- Very low on-resistance $R_{DS(on)}$
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED!
100% ΔVds TESTED!

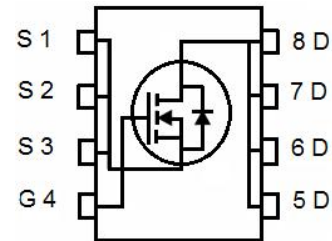
DFN 5X6



Top View



Bottom View



Schematic Diagram

Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|----------------|------------|----------------|-----------|------------|----------|
| P1570GU | NCEP1570GU | DFN5X6-8L | - | - | - |

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|--------------------------------------------------|--------------------|------------|------|
| Drain-Source Voltage | V_{DS} | 150 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous | I_D | 70 | A |
| Drain Current-Continuous ($T_C = 100^\circ C$) | $I_D(100^\circ C)$ | 49 | A |
| Pulsed Drain Current | I_{DM} | 280 | A |
| Maximum Power Dissipation | P_D | 160 | W |
| Derating factor | | 1.28 | W/°C |
| Single pulse avalanche energy (Note 1) | E_{AS} | 583 | mJ |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 150 | °C |

Thermal Characteristic

| | | | |
|--------------------------------------|-----------------|------|------|
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 0.78 | °C/W |
|--------------------------------------|-----------------|------|------|

Electrical Characteristics (T_A=25°C unless otherwise noted)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|----------------------------------------------------------|---------------------|---------------------------------------------------------------------------------------|-----|------|------|------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =250μA | 150 | - | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =150V, V _{GS} =0V | - | - | 1 | μA |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| On Characteristics | | | | | | |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =250μA | 2.0 | 3.0 | 4.0 | V |
| Drain-Source On-State Resistance | R _{DS(ON)} | V _{GS} =10V, I _D =35A | - | 13.5 | 15 | mΩ |
| Forward Transconductance | g _{FS} | V _{DS} =5V, I _D =35A | - | 58 | - | S |
| Dynamic Characteristics | | | | | | |
| Input Capacitance | C _{ISS} | V _{DS} =75V, V _{GS} =0V, F=1.0MHz | - | 2000 | - | PF |
| Output Capacitance | C _{OSS} | | - | 280 | - | PF |
| Reverse Transfer Capacitance | C _{RSS} | | - | 16 | - | PF |
| Switching Characteristics <small>(Note 2)</small> | | | | | | |
| Turn-on Delay Time | t _{d(on)} | V _{DD} =75V, I _D =35A V _{GS} =10V, R _G =3Ω | - | 12.5 | - | nS |
| Turn-on Rise Time | t _r | | - | 3.8 | - | nS |
| Turn-Off Delay Time | t _{d(off)} | | - | 14 | - | nS |
| Turn-Off Fall Time | t _f | | - | 3.5 | - | nS |
| Total Gate Charge | Q _g | V _{DS} =75V, I _D =35A, V _{GS} =10V | - | 35 | - | nC |
| Gate-Source Charge | Q _{gs} | | - | 11.8 | - | nC |
| Gate-Drain Charge | Q _{gd} | | - | 9.9 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage | V _{SD} | V _{GS} =0V, I _S =35A | - | - | 1.2 | V |
| Diode Forward Current | I _S | | - | - | 70 | A |
| Reverse Recovery Time | t _{rr} | T _J = 25°C, I _F = 35A di/dt = 100A/μs | - | 47 | - | nS |
| Reverse Recovery Charge | Q _{rr} | | - | 65 | - | nC |

Notes:

1. E_{AS} condition : T_J=25°C, V_{DD}=50V, V_G=10V, L=0.5mH, R_G=25Ω
2. Guaranteed by design, not subject to production
3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_J(MAX)=150° C. The SOA curve provides a single pulse rating.

Typical Electrical and Thermal Characteristics

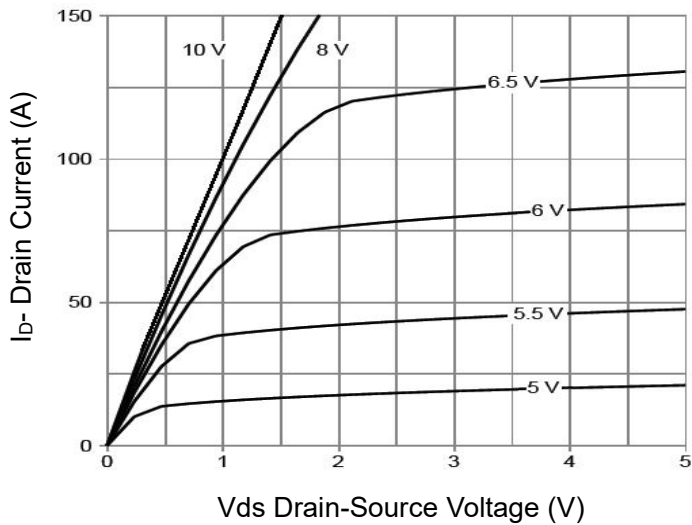


Figure 1 Output Characteristics

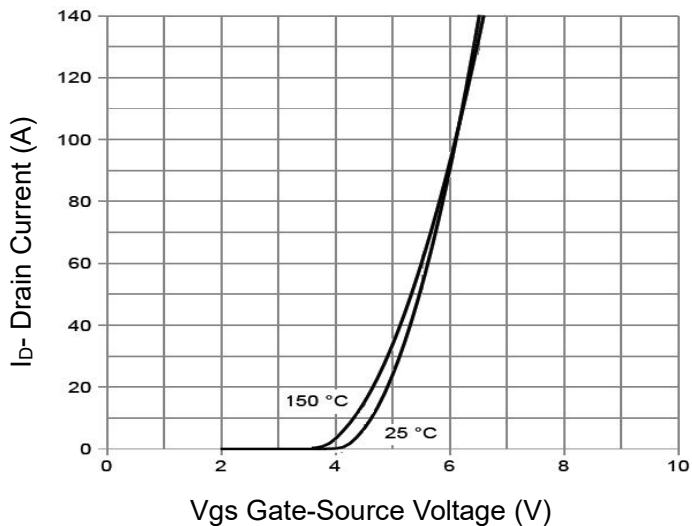


Figure 2 Transfer Characteristics

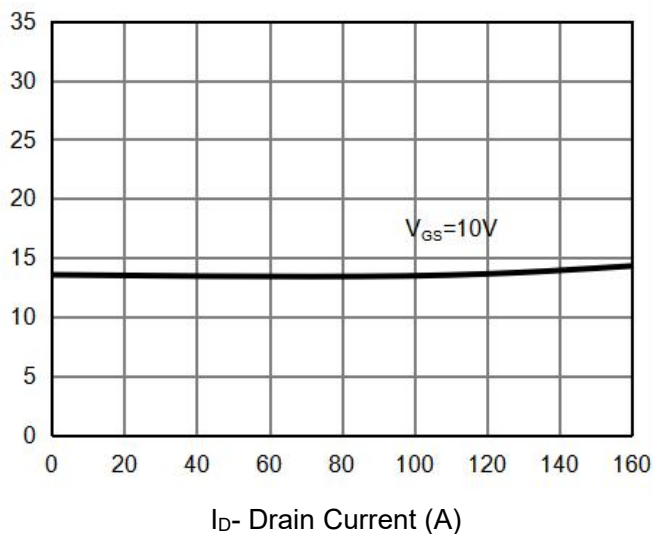


Figure 3 $R_{DS(on)}$ - Drain Current

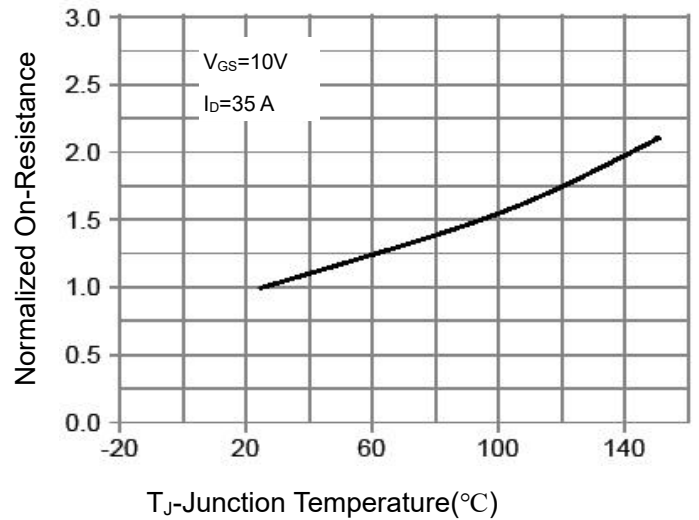


Figure 4 $R_{DS(on)}$ -Junction Temperature

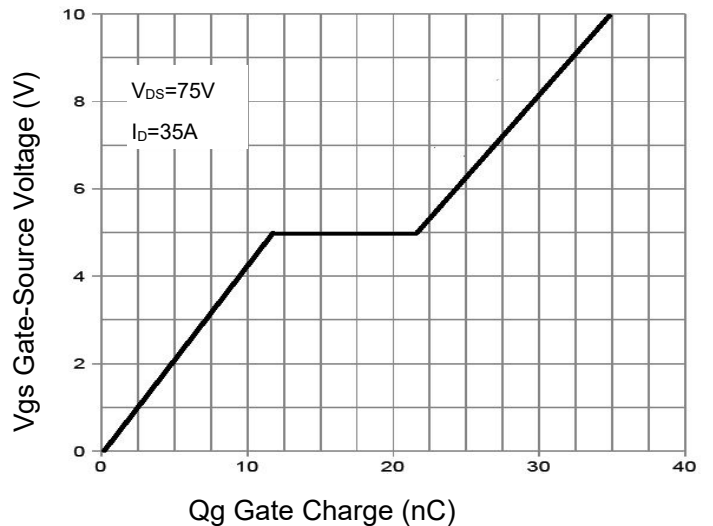


Figure 5 Gate Charge

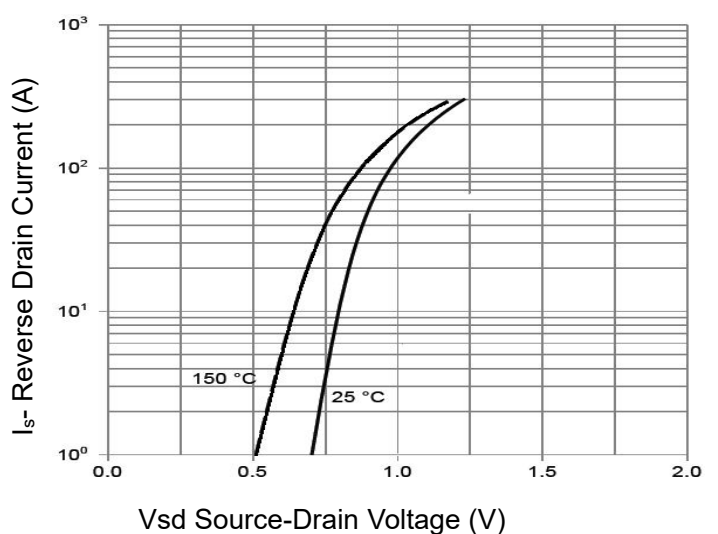
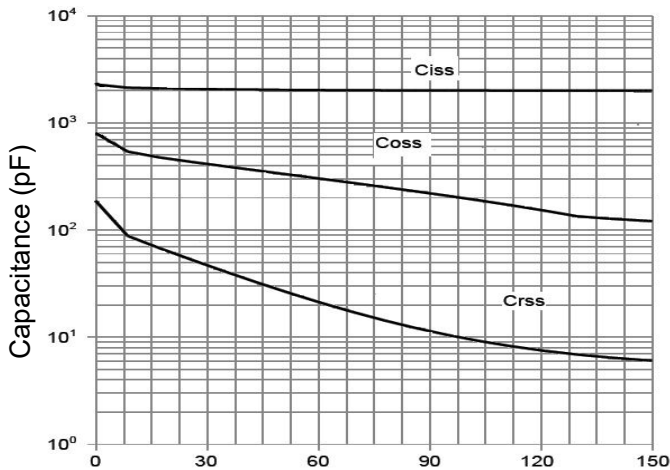
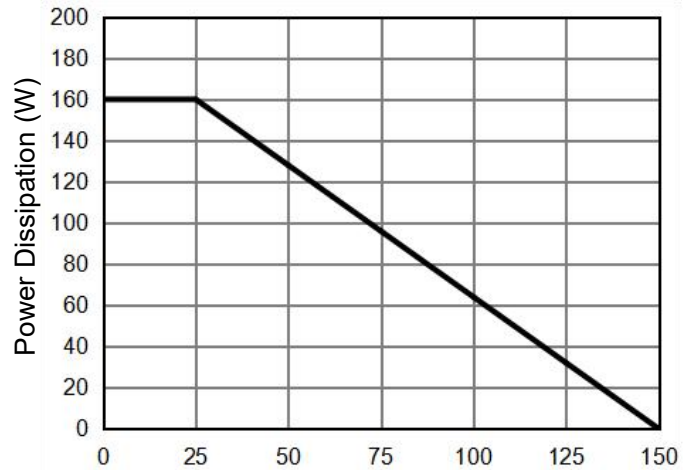


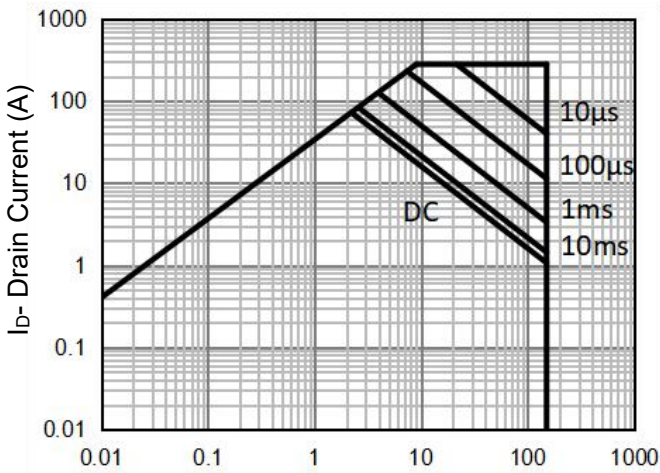
Figure 6 Source- Drain Diode Forward



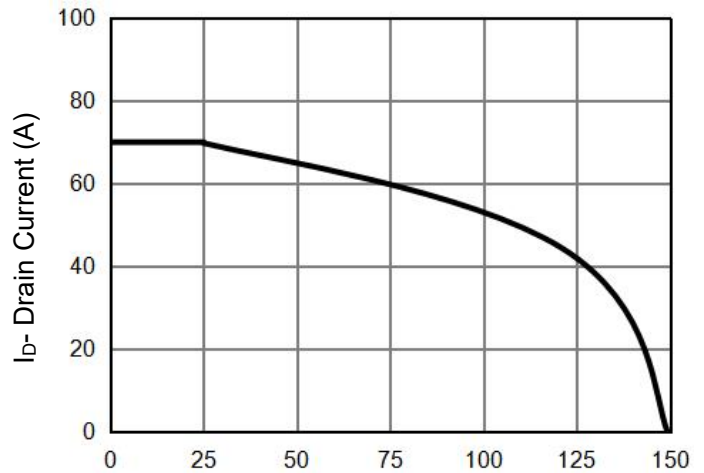
Vds Drain-Source Voltage (V)
Figure 7 Capacitance vs Vds



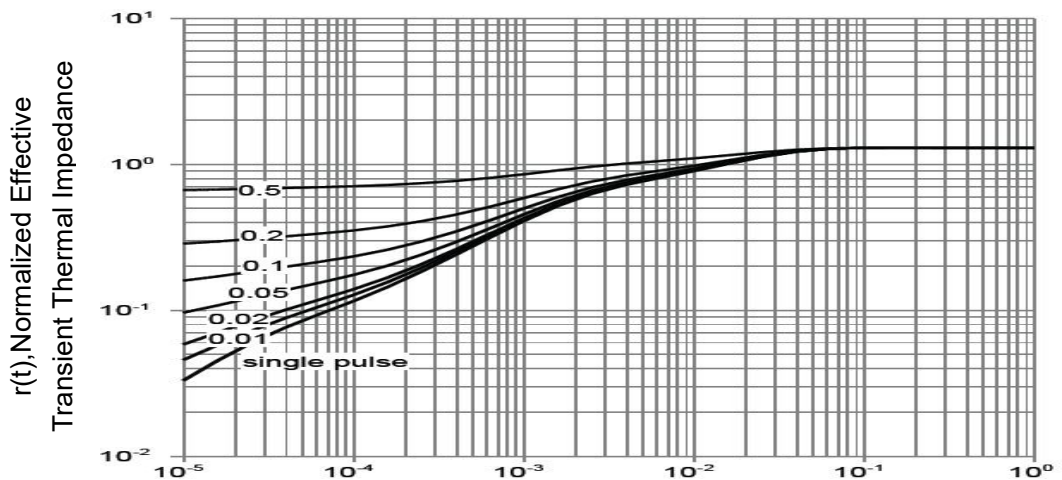
T_J-Junction Temperature(°C)
Figure 9 Power De-rating



Vds Drain-Source Voltage (V)
Figure 8 Safe Operation Area (Note 3)

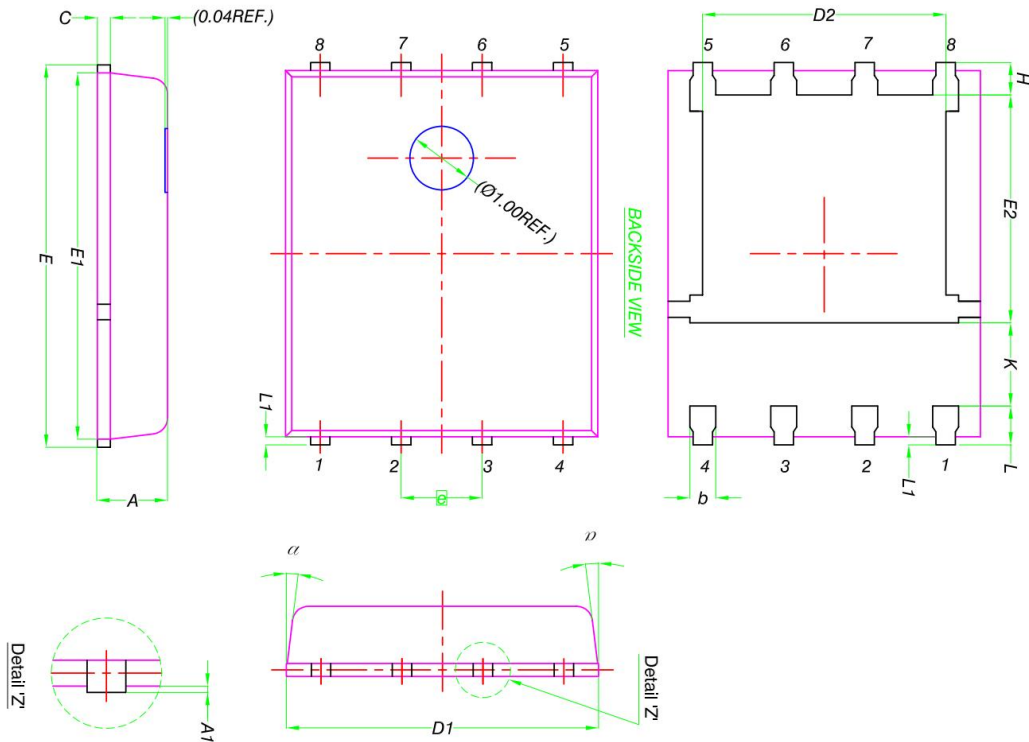


T_J-Junction Temperature (°C)
Figure 10 Current De-rating

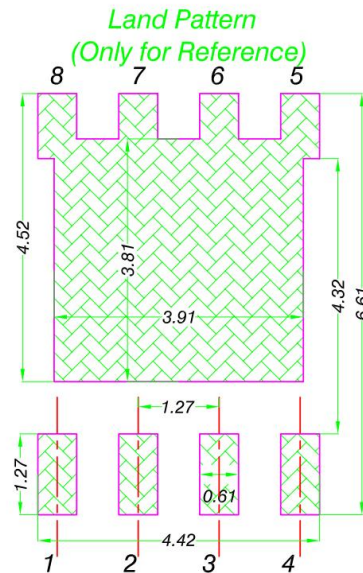


Square Wave Pulse Duration(sec)
Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



| DIM. | MILLIMETERS | | |
|----------|-------------|------|------|
| | MIN. | NOM. | MAX. |
| A | 0.90 | 1.00 | 1.10 |
| A1 | 0 | - | 0.05 |
| b | 0.33 | 0.41 | 0.51 |
| C | 0.20 | 0.25 | 0.30 |
| D1 | 4.80 | 4.90 | 5.00 |
| D2 | 3.61 | 3.81 | 3.96 |
| E | 5.90 | 6.00 | 6.10 |
| E1 | 5.70 | 5.75 | 5.80 |
| E2 | 3.38 | 3.58 | 3.78 |
| e | 1.27 BSC | | |
| H | 0.41 | 0.51 | 0.61 |
| K | 1.10 | - | - |
| L | 0.51 | 0.61 | 0.71 |
| L1 | 0.06 | 0.13 | 0.20 |
| α | 0° | - | 12° |



Note:

1. All Dimension Are In mm.
2. Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs.
Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
3. Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
4. The Package Top May Be Smaller Than The Package Bottom.

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