

NCE N-Channel Super Trench II Power MOSFET

Description

The NCEP18N10AQ uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

General Features

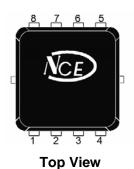
• V_{DS} =100V,I_D =35A

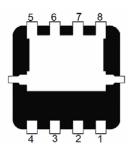
 $R_{DS(ON)}$ =16.0m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =18.0m Ω (typical) @ V_{GS} =4.5V

- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating

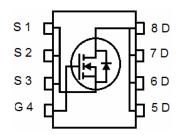
100% UIS TESTED! 100% ΔVds TESTED!

DFN 3.3X3.3





Bottom View



Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP18N10AQ	NCEP18N10AQ	DFN3.3X3.3-8L	-	-	-

Absolute Maximum Ratings (T_c=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	100	V	
Gate-Source Voltage	V _G S	±20	V	
Drain Current-Continuous	I _D	35	Α	
Drain Current-Continuous(T _C =100°C)	I _D (100℃)	25	А	
Pulsed Drain Current	I _{DM}	140	А	
Maximum Power Dissipation	P _D	48	W	
Derating factor		0.39	W/°C	
Single pulse avalanche energy (Note 5)	E _{AS}	115	mJ	
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 150	$^{\circ}$	

Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{ heta JC}$	2.6	°C/W

NCEP18N10AQ

Electrical Characteristics (T_C=25 °C unless otherwise noted)

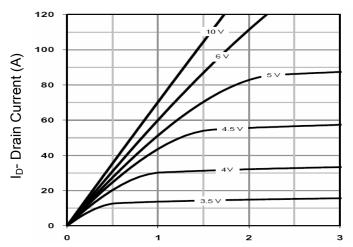
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	100		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			'			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.2	1.7	2.2	V
Desir Course On State Resistance	Б	V _{GS} =10V, I _D =17.5A	-	0	mΩ	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =17.5A	-	20	22	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =17.5A	20	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}		-	1719.5	-	PF
Output Capacitance	C _{oss}	V_{DS} =50V, V_{GS} =0V,	-	147.4	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	16	-	PF
Switching Characteristics (Note 4)			•			•
Turn-on Delay Time	t _{d(on)}		-	14	-	nS
Turn-on Rise Time	t _r	V_{DD} =50V, I_{D} =17.5A	-	16	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	28	-	nS
Turn-Off Fall Time	t _f		-	8	-	nS
Total Gate Charge	Qg)/ F0)/ 47.FA	-	37.6	-	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=50V,I_{D}=17.5A,$	-	6.5		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	9.5		nC
Drain-Source Diode Characteristics	1		Ī		l_	
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =17.5A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	35	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C$, $I_F = 17.5A$	-	43	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	90	-	nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V $_{\text{DD}}$ =50 V,V $_{\text{G}}$ =10 V,L=0.5 mH,Rg=25 Ω

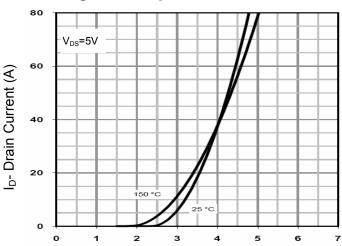


Typical Electrical and Thermal Characteristics



Vds Drain-Source Voltage (V)

Figure 1 Output Characteristics



Vgs Gate-Source Voltage (V)

Figure 2 Transfer Characteristics

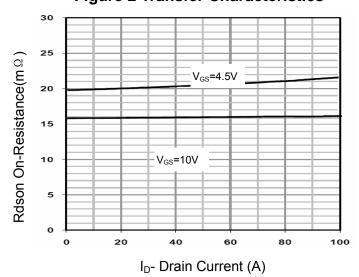
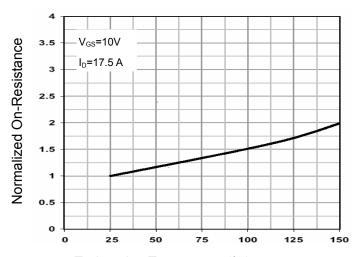


Figure 3 Rdson- Drain Current



 T_J -Junction Temperature($^{\circ}$ C)

Figure 4 Rdson-Junction Temperature

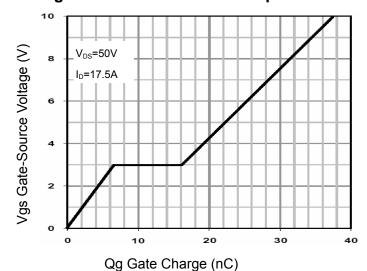
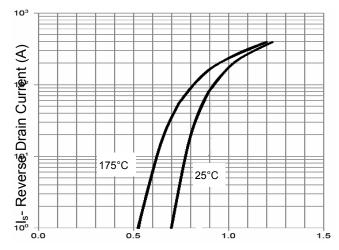


Figure 5 Gate Charge



Vsd Source-Drain Voltage (V)

Figure 6 Source- Drain Diode Forward



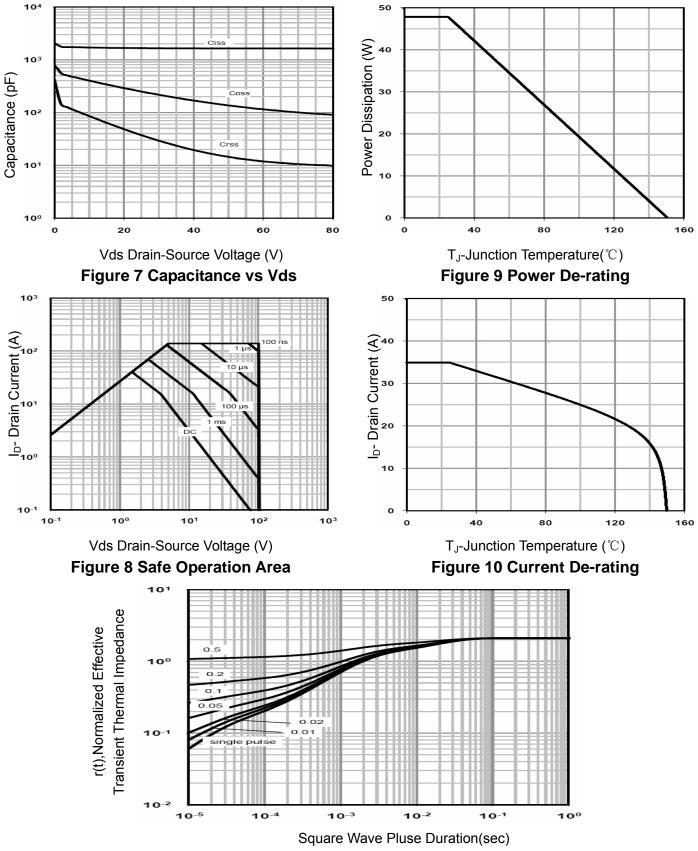
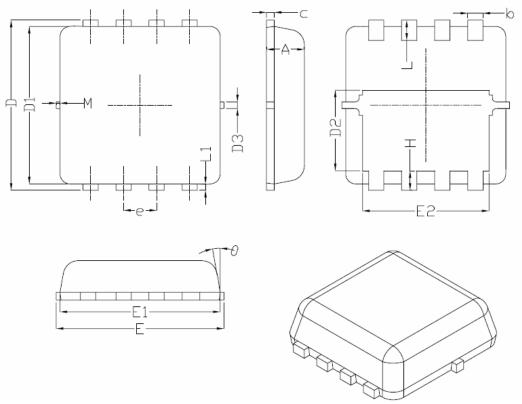


Figure 11 Normalized Maximum Transient Thermal Impedance



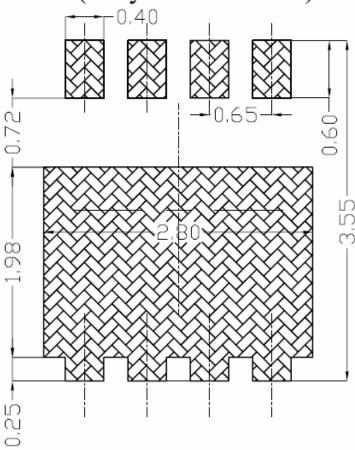
DFN3.3X3.3-8L Package Information



	Dimensions In Millimeters				
Symbol					
		Nom.	Max.		
Α	0.70	0.75	0.80		
b	0.25	0.30	0.35		
С	0.10	0.15	0.25		
D	3.25	3.35	3.45		
D1	3.00	3.10	3.20		
D2	1.48	1.58	1.68		
D3	-	0.13	-		
E	3.20	3.30	3.40		
E1	3.00	3.15	3.20		
E2	2.39	2.49	2.59		
е	0.65BSC				
Н	0.30	0.39	0.50		
L	0.30	0.40	0.50		
L1	-	0.13	-		
M	*	*	0.15		
θ		10°	12 [°]		



Land Pattern (Only for Reference)



http://www.ncepower.com

NCEP18N10AQ

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