

NCE N-Channel Super Trench Power MOSFET

Description

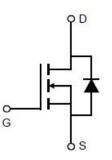
The NCEP30T17GU uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS}(\text{ON})}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

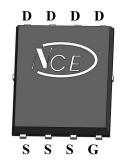
- V_{DS} =30V, I_D =170A $R_{DS(ON)}$ =0.97m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =1.25m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

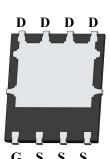
Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Schematic Diagram





Top View

Bottom View

100% UIS TESTED!

100% ΔVds TESTED!

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P30T17GU	NCEP30T17GU	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (T_c=25 ℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous (Silicon Limited)	I _D	170	Α
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	125	А
Pulsed Drain Current	I _{DM}	680	А
Maximum Power Dissipation	P _D	135	W
Derating factor		1.08	W/℃
Single pulse avalanche energy (Note 1)	E _{AS}	1350	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$

NCEP30T17GU

Thermal Characteristic

Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	0.926	°C/W	
	000	***		1

Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Sy	ymbol	Condition	Min	Тур	Max	Unit
Off Characteristics							
Drain-Source Breakdown Voltage	E	3V _{DSS}	V _{GS} =0V I _D =250μA	30		-	V
Zana Cata Valtana Duain Cumant		TJ=25℃	V_{DS} =30 V , V_{GS} =0 V	-	-	1	μA
ero Gate Voltage Drain Current	I _{DSS}	TJ=55℃		-	-	1.5	μA
eta Badul askana Cumant			V _{GS} =±5V,V _{DS} =0V	-	-	±80	nA
Gate-Body Leakage Current		I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics	·						
Gate Threshold Voltage	te Threshold Voltage V _{GS}		$V_{DS}=V_{GS},I_{D}=250\mu A$	1.0	1.5	2.0	V
Drain-Source On-State Resistance		,	V_{GS} =10V, I_D =20A	0.75	0.97	1.2	mΩ
Drain-Source On-State Resistance		R _{DS(ON)}	V _{GS} =4.5V, I _D =20A	1	1.25	1.5	mΩ
Forward Transconductance		g FS	V _{DS} =5V,I _D =20A		80	-	S
Dynamic Characteristics	·			•			
Input Capacitance		C _{lss}	V _{DS} =15V,V _{GS} =0V, F=1.0MHz	-	5300	6890	PF
Output Capacitance		Coss		-	1800	2600	PF
Reverse Transfer Capacitance		C _{rss}	F-1.UIVITZ	-	100	200	PF
Switching Characteristics	·			•		•	•
Turn-on Delay Time ^(Note 2)	ote 2)	t _{d(on)}		-	12	-	nS
Turn-on Rise Time ^(Note 2)			V_{DD} =15 V , I_D =20 A	-	6.5	-	nS
Turn-Off Delay Time ^(Note 2)		t _{d(off)}	$V_{GS}\text{=}10V,R_{G}\text{=}1.6\Omega$	-	48	-	nS
Turn-Off Fall Time ^(Note 2)		t _f		-	7.5	-	nS
Total Gate Charge	ate-Source Charge Q _{gs}		V _{DS} =15V,I _D =20A,	-	90	126	nC
Gate-Source Charge				-	12	18	nC
Gate-Drain Charge			V _{GS} =10V	-	13	19.5	nC
Drain-Source Diode Characteristics							
Diode Forward Voltage	V _{SD}		V _{GS} =0V,I _S =20A	-	-	1.2	V
Diode Forward Current	Is			-	-	170	Α
Reverse Recovery Time t _{rr}		t _{rr}	T _J = 25°C, I _F = I _S	-	-	30	nS
Reverse Recovery Charge		Qrr	$di/dt = 100A/\mu s$	-	-	110	nC

Notes:

- 1. E_{AS} condition : Tj=25 $^{\circ}\text{C}$,V_DD=20V,V_G=10V,L=0.5mH,Rg=25 Ω
- 2. Guaranteed by design, not subject to production
- 3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsin k, assuming a maximum junction temperature of TJ(MAX)=150° C. The SOA curve provides a single pulse rating.

Typical Electrical and Thermal Characteristics

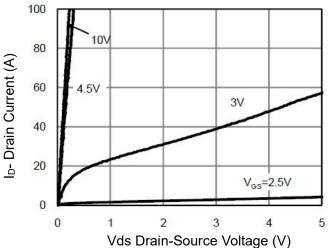


Figure 1 Output Characteristics

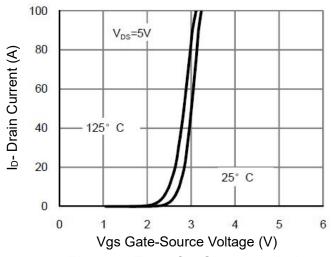


Figure 2 Transfer Characteristics

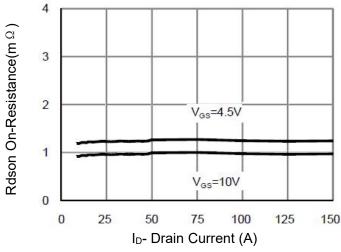


Figure 3 Rdson-Drain Current

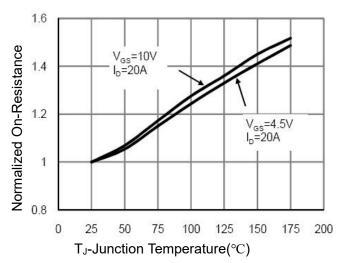


Figure 4 Rdson-Junction Temperature

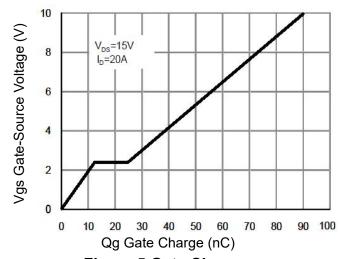


Figure 5 Gate Charge

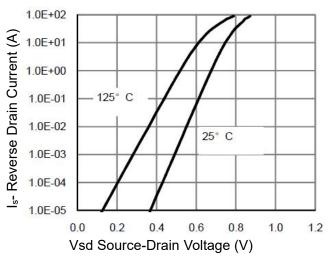


Figure 6 Source- Drain Diode Forward

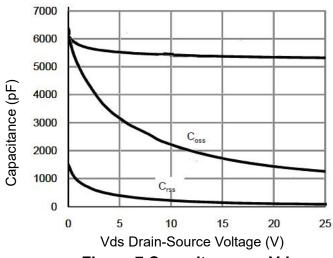


Figure 7 Capacitance vs Vds

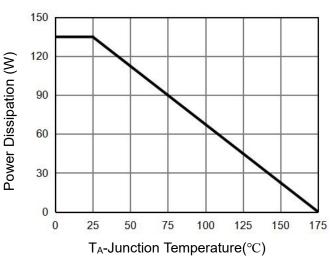


Figure 9 Power De-rating

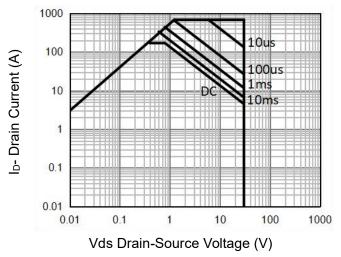


Figure 8 Safe Operation Area (Note3)

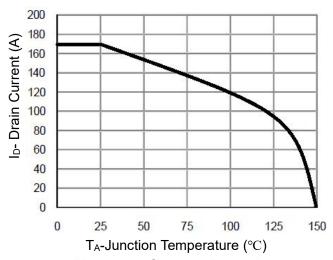


Figure 10 Current De-rating

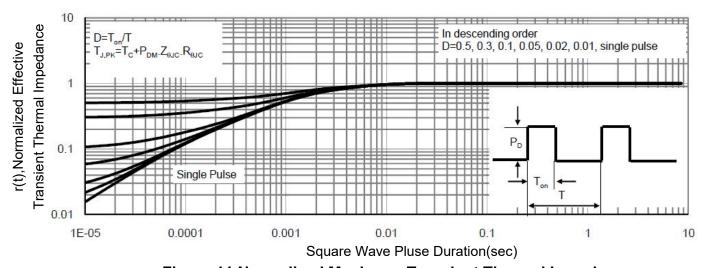
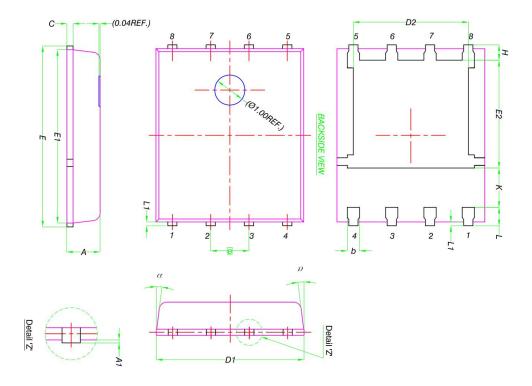
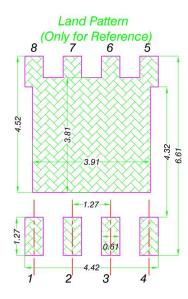


Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L(G) Package Information



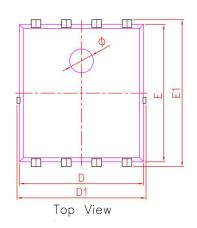
	MILLIMETERS				
DIM.	MIN.	NOM.	MAX.		
Α	A 0.90		1.10		
A1	0	×	0.05		
b	0.33	0.41	0.51		
С	0.20	0.25	0.30		
D1	4.80	4.90	5.00		
D2	3.61	3.81	3.96		
Ε	5.90	6.00	6.10		
E1	5.70	5.75	5.80		
E2	3.38	3.58	3.78		
е		1.27 BSC			
Н	0.41	0.51	0.61		
K	1.10	-	-		
L	0.51	0.61	0.71		
L1	0.06	0.13	0.20		
α	0°	-	12°		

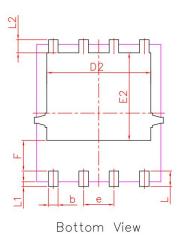


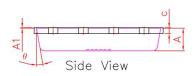
Note:

- 1. All Dimension Are In mm.
- Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
- Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.

DFN5X6-8L(E) Package Information







PDFN5X6-8L					
DIM.	MIN.	NOM.	MAX.		
Α	0.90	0.95	1.00		
A1	0.00	0.02	0.05		
b	0.35	0.40	0.50		
С	0.20	0.25	0.30		
D	5.10	5.20	5.30		
D1	5.10	5.40	5.50		
D2	4.25	4.35	4.45		
е	1.27 BSC				
Е	5.70	5.75	5.80		
E1	6.00	6.15	6.30		
E2	3.57	3.67	3.77		
F	1.18	1.28	1.38		
L	0.55	0.65	0.75		
L1 0.15		0.20	0.25		
L2	L2 0.45		0.65		
ø	0.90	1.00	1.10		
Θ	8°	10°	12°		
All dimensions in millimeters					

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