

NCE N-Channel Super Trench Power MOSFET

Description

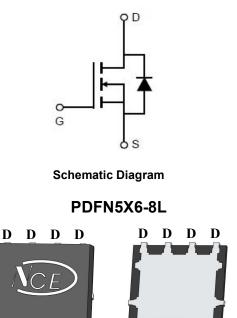
The NCEP40T20GU uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{\text{DS}(\text{ON})}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

General Features

- V_{DS} =40V, I_D =200A $R_{DS(ON)}$ =0.9m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =1.15m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification





Bottom View

100% UIS TESTED! 100% ΔVds TESTED!

Top View

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P40T20GU	NCEP40T20GU	PDFN5X6-8L	Ø330mm	12mm	5000units

Absolute Maximum Ratings (Tc=25°Cunless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	40	V
Gate-Source Voltage	V _G s	±20	V
Drain Current-Continuous (Silicon Limited)	I _D	200	Α
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	150	Α
Pulsed Drain Current	I _{DM}	800	А
Maximum Power Dissipation	P _D	180	W
Derating factor		1.44	W/°C
Single pulse avalanche energy (Note 1)	E _{AS}	1800	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$

Thermal Characteristic

Thermal Resistance,Junction-to-Case	R _{θJC}	0.67	°C/W	
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						•
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	40	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics						•
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\mu A$	1.0	1.5	2.2	V
Davis Course On Otata Basistana		V _{GS} =10V, I _D =100A	-	0.9	1.1	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =100A	-	1.15	1.5	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =100A		90	-	S
Dynamic Characteristics						•
Input Capacitance	C _{lss}	V 00VVV 0V	-	8085	-	pF
Output Capacitance	Coss	$V_{DS}=20V, V_{GS}=0V,$	-	2123	-	pF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	121	-	pF
Switching Characteristics (Note 2)						•
Turn-on Delay Time	t _{d(on)}		-	13	-	nS
Turn-on Rise Time	t _r	V_{DD} =20 V , I_{D} =100 A	-	8	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{G} =1.6 Ω	-	55	-	nS
Turn-Off Fall Time	t _f		-	10	-	nS
Total Gate Charge	Qg	V 00V/1 400A	-	137	-	nC
Gate-Source Charge	Q _{gs}	V _{DS} =20V,I _D =100A,	-	19	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	14	-	nC
Drain-Source Diode Characteristics	,				'	
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =100A	-	-	1.2	V
Diode Forward Current	Is		-	-	200	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = 100A	-	35	-	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs		120		nC

Notes:

^{1.} EAS condition : Tj=25 $^{\circ}\text{C}\text{,V}_{\text{DD}}\text{=}20\text{V,V}_{\text{G}}\text{=}10\text{V,L=}0.5\text{mH,Rg=}25\Omega$

^{2.} Guaranteed by design, not subject to production

^{3.} These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of TJ(MAX)=150°C. The SOA curve provides a single pulse rating.



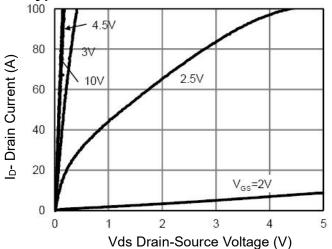


Figure 1 Output Characteristics

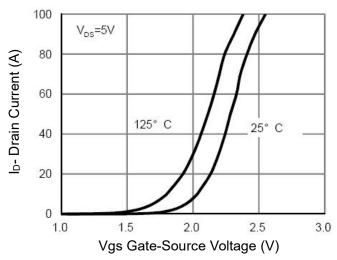


Figure 2 Transfer Characteristics

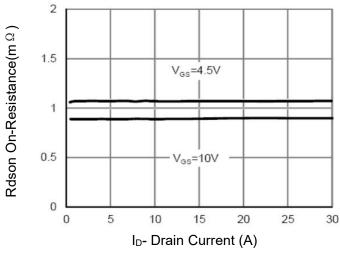


Figure 3 Rdson- Drain Current

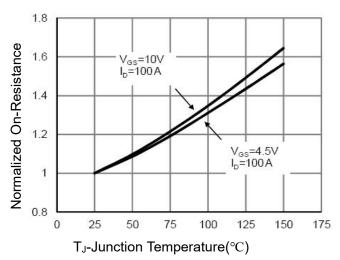


Figure 4 Rdson-Junction Temperature

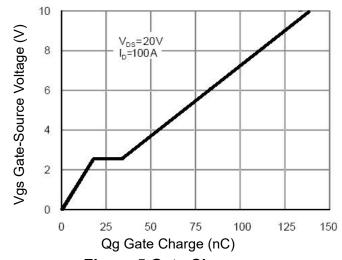


Figure 5 Gate Charge

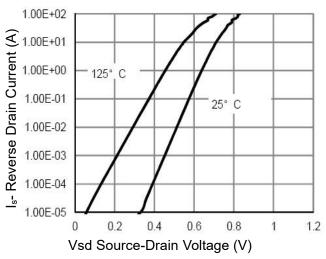


Figure 6 Source- Drain Diode Forward

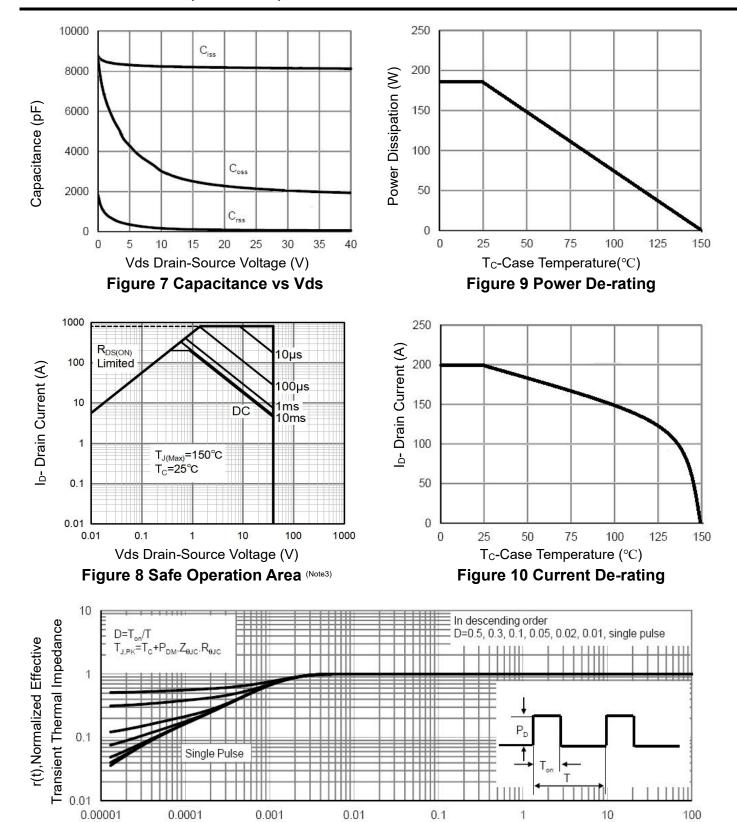
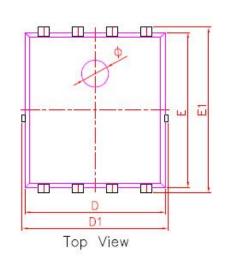
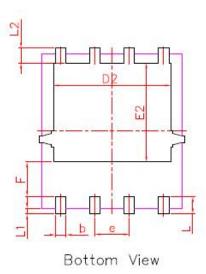


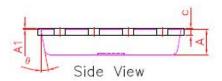
Figure 11 Normalized Maximum Transient Thermal Impedance

Square Wave Pluse Duration(sec)

PDFN5X6-8L(E) Package Information

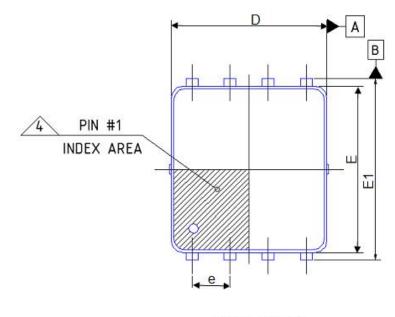


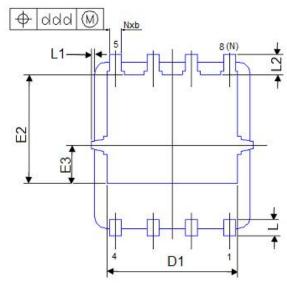




DIM.	MIN.	NOM.	MAX.
Α	0.90	0.95	1.00
A1	0.00	0.02	0.05
b	0.35	0.40	0.50
С	0.20	0.25	0.30
D	5.10	5.20	5.30
D1	5.10	5.40	5.50
D2	4.25	4.35	4.45
е		1.27 BSC	
Ε	5.70	5.75	5.80
E1	6.00	6.15	6.30
E2	3.57	3.67	3.77
F	1.18	1.28	1.38
L.	0.55	0.65	0.75
L1	0.15	0.20	0.25
L2	0.45	0.55	0.65
Ø	0.90	1.00	1.10
Θ	8.	10"	12*

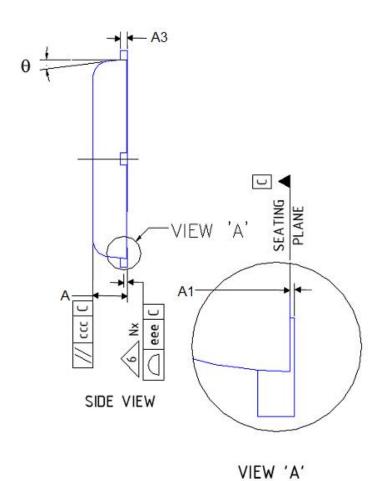
DFN5X6-8L(f) Package Information





TOP VIEW

BOTTOM VIEW



74.				
Thickness Symbol		NOTE		
mbol 3	MINIMUM NOMINAL MAXIMU		MAXIMUM	
Α	0.85	0.95	1.00	
A1	0.00		0.05	
A3		0.2 Ref	1	
b	0.30	0.40	0.50	
D	5.10	5.20	5.30	
E	5.45	5.55	5.65	
е		1.27 BSC		
D1	4.25	4.35	4.45	
E1	5.95	6.05	6.15	
E2	3.525	3.625	3.725	
E3	1.175	1.275	1.375	
L	0.45	0.55	0.65	
L1	0		0.15	
L2		0.68 REF		
θ	0°		10°	
aaa		0.05		
bbb	0.10			
ССС	0.10			
ddd	0.05			
eee	0.08			
N	8			
ND	4			
NOTES		1,2		

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