



NCE N-Channel Enhancement Mode Power MOSFET

Description

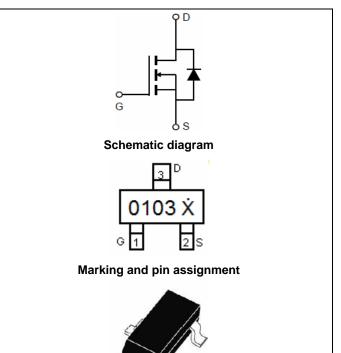
The NCE0103 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

General Features

- $V_{DS} = 100V, I_D = 3A$ $R_{DS(ON)} < 160m\Omega @ V_{GS} = 10V$ (Typ:136m Ω) $R_{DS(ON)} < 170m\Omega @ V_{GS} = 4.5V$ (Typ:140m Ω)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Excellent package for good heat dissipation

Application

- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply



SOT-23 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
0103 X	NCE0103	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	100	V
Gate-Source Voltage	Vgs	±20	V
Drain Current-Continuous	I _D	3	А
Drain Current-Pulsed (Note 1)	I _{DM}	20	A
Maximum Power Dissipation	PD	1.5	W
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 To 175	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	100	°C /W
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Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V _{GS} =0V I _D =250µA	100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =100V, V_{GS} =0V	-	-	1	μA



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NCE0103

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I _{GSS}	V_{GS} =±20V, V_{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.0	1.5	2.0	V
Drain Course On State Desistance	_	V_{GS} =10V, I _D =3A	-	136	160	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =3A	-	140	170	
Forward Transconductance	g fs	V _{DS} =5V,I _D =3A	-	5	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}		-	650	-	PF
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V,	-	24	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	20	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t _{d(on)}		-	6	-	nS
Turn-on Rise Time	tr	V_{DD} =50V, R _L =19 Ω	-	4	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10V, R_{G} =3 Ω	-	20	-	nS
Turn-Off Fall Time	t _f		-	4	-	nS
Total Gate Charge	Qg	N/ F0\/L 0A	-	20		nC
Gate-Source Charge	Q _{gs}	V_{DS} =50V,I _D =3A,	-	2.1	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	3.3	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =3A	-	-	1.2	V
Diode Forward Current (Note 2)	I _S		-	-	3	А

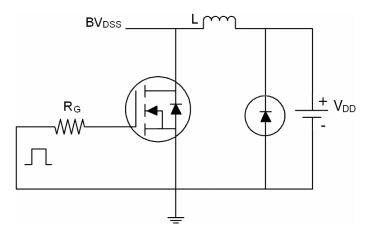
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, t \leq 10 sec.
- **3.** Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to productio

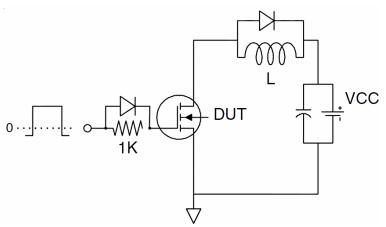


Test Circuit

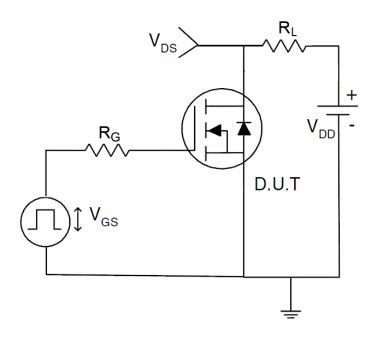
1) E_{AS} test circuit



2) Gate charge test circuit



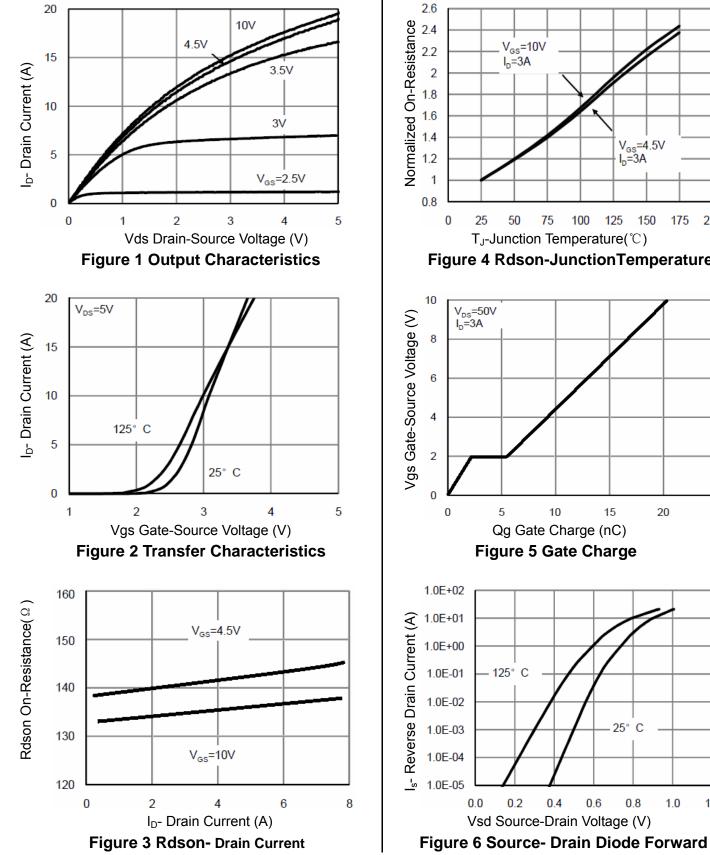
3) Switch Time Test Circuit

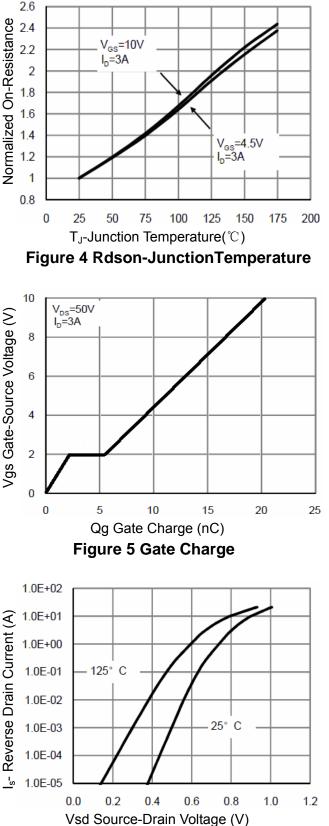




Pb-Free Product

Typical Electrical and Thermal Characteristics (Curves)







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Pb-Free Product

NCE0103

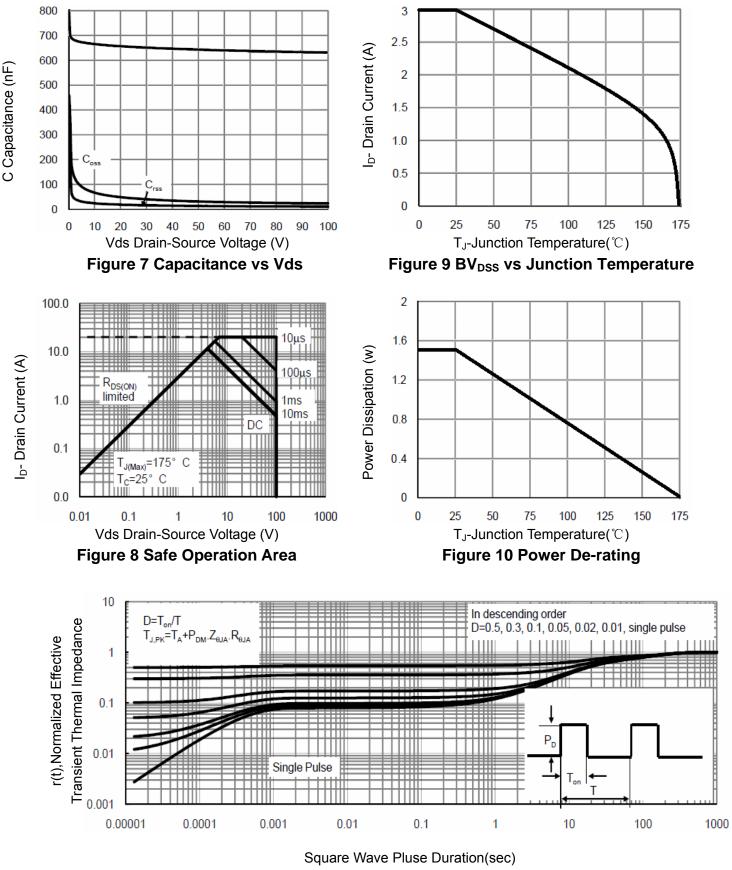
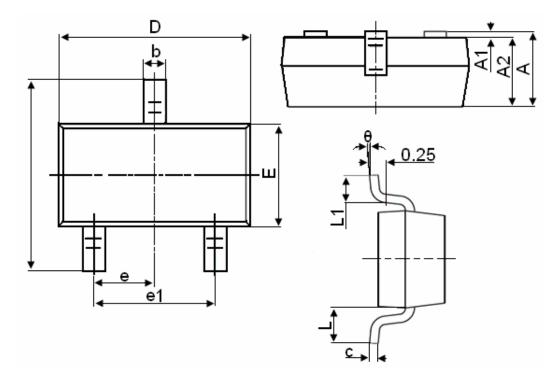


Figure 11 Normalized Maximum Transient Thermal Impedance



SOT-23 Package Information



Symbol		Dimensions in Millimeters		
Symbol	MIN.	MAX.		
A	0.900	1.150		
A1	0.000	0.100		
A2	0.900	1.050		
b	0.300	0.500		
с	0.080	0.150		
D	2.800	3.000		
E	1.200	1.400		
E1	2.250	2.550		
е		0.950TYP		
e1	1.800	2.000		
L		0.550REF		
L1	0.300	0.500		
θ	0°	8°		

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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