NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE2302B uses advanced trench technology to provide excellent $R_{\rm DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

General Features

• $V_{DS} = 20V, I_D = 3.3A$

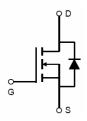
 $R_{DS(ON)}$ < 60m Ω @ V_{GS} =2.5V

 $R_{DS(ON)}$ < 45m Ω @ V_{GS} =4.5V

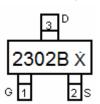
- High power and current handing capability
- Lead free product is acquired
- Surface mount package

Application

- Battery protection
- Load switch
- Power management



Schematic diagram



Marking and pin assignment



SOT-23 top view

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
2302B X	NCE2302B	SOT-23	Ø180mm	8 mm	3000 units

Absolute Maximum Ratings (T_A=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	20	V
Gate-Source Voltage	V _{GS}	±12	V
Drain Current-Continuous	I _D	3.3	Α
Drain Current-Pulsed (Note 1)	I _{DM}	16	Α
Maximum Power Dissipation	P _D	0.9	W
Operating Junction and Storage Temperature Range	T_{J},T_{STG}	-55 To 150	$^{\circ}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	139	°C/W

Electrical Characteristics (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	20	22	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V,V _{GS} =0V	-	-	1	μΑ



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Parameter	Symbol	Condition	Min	Тур	Max	Unit
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±12V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.5	0.75	1.2	V
Drain Course On Otata Basistanas	Б	V _{GS} =2.5V, I _D =2.8A	-	35	60	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =3A	-	29	45	mΩ
Forward Transconductance	g fs	V_{DS} =5 V , I_{D} =3 A	-	8	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C _{lss}	\/ -40\/\/ -0\/	-	260	-	PF
Output Capacitance	Coss	$V_{DS}=10V, V_{GS}=0V,$	-	48	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	27	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}	V_{DD} =10V, R_L =3.3 Ω V_{GS} =4.5V, R_{GEN} =6 Ω	-	2.5	-	nS
Turn-on Rise Time	t _r		-	3.2	-	nS
Turn-Off Delay Time	t _{d(off)}		-	21	-	nS
Turn-Off Fall Time	t _f		-	3	-	nS
Total Gate Charge	Qg	V _{DS} =10V,I _D =3A,	-	2.9	5	nC
Gate-Source Charge	Q _{gs}		-	0.4	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =4.5V	-	0.6	-	nC
Drain-Source Diode Characteristics	,					•
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =3.3A	-	0.75	1.2	V
Diode Forward Current (Note 2)	Is		-	-	3.3	Α

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- **3.** Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production

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Typical Electrical and Thermal Characteristics

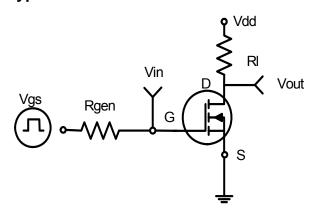


Figure 1:Switching Test Circuit

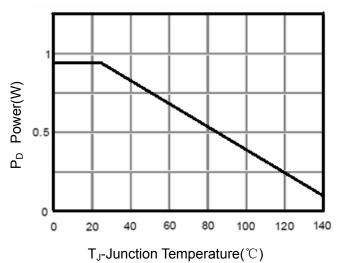


Figure 3 Power Dissipation

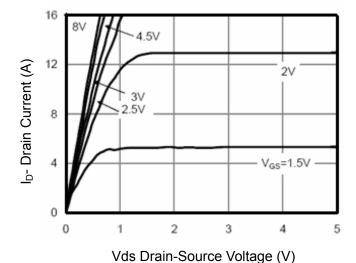


Figure 5 Output Characteristics

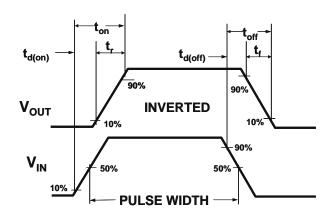


Figure 2:Switching Waveforms

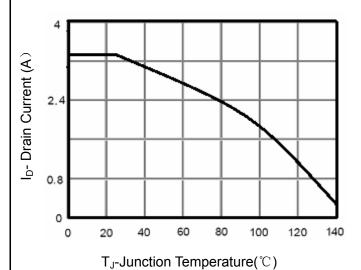


Figure 4 Drain Current

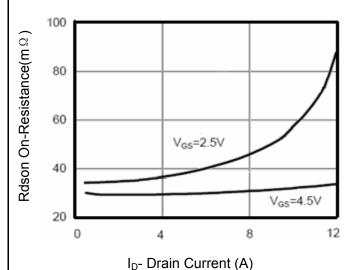


Figure 6 Drain-Source On-Resistance



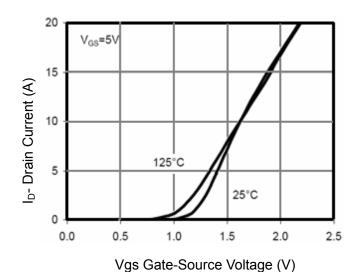
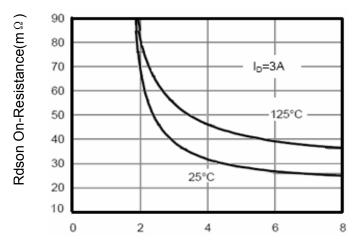


Figure 7 Transfer Characteristics



Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs

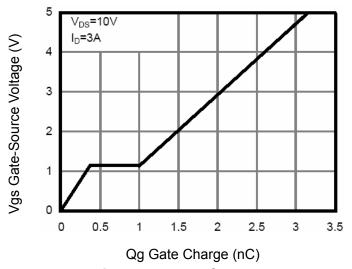
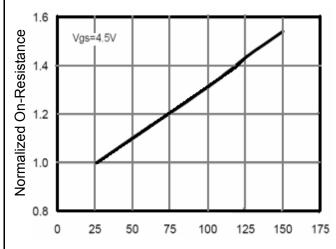
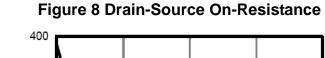
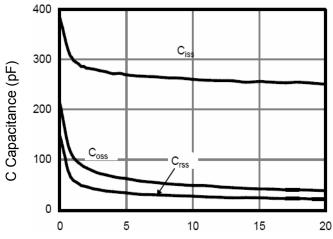


Figure 11 Gate Charge

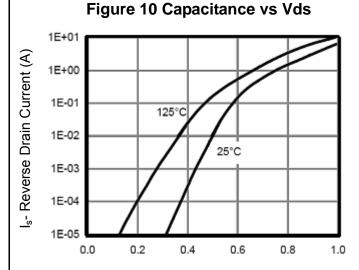


 T_J -Junction Temperature($^{\circ}$ C)





Vds Drain-Source Voltage (V)



Vsd Source-Drain Voltage (V) Figure 12 Source- Drain Diode Forward

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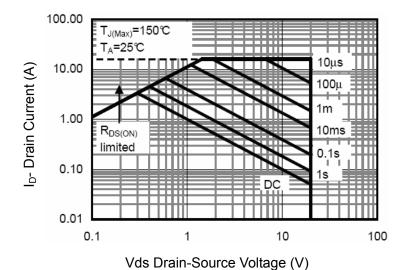


Figure 13 Safe Operation Area

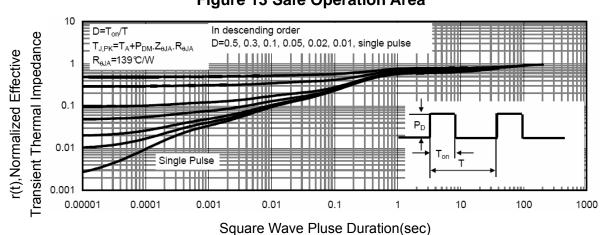
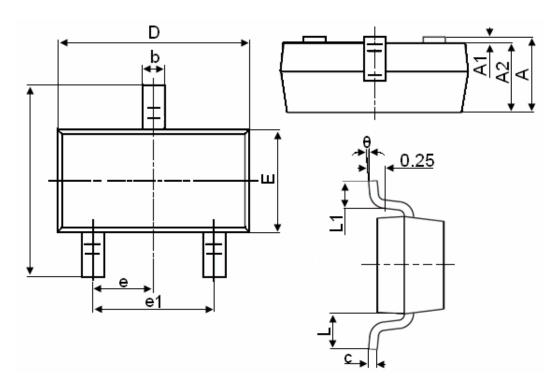


Figure 14 Normalized Maximum Transient Thermal Impedance

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SOT-23 Package Information



Symbol	Dimensions in Millimeters			
Symbol	MIN.	MAX.		
Α	0.900	1.150		
A1	0.000	0.100		
A2	0.900	1.050		
b	0.300	0.500		
С	0.080	0.150		
D	2.800	3.000		
Е	1.200	1.400		
E1	2.250	2.550		
е		0.950TYP		
e1	1.800	2.000		
L	0.550REF			
L1	0.300	0.500		
θ	0°	8°		

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.



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