

1200V, 50A, Trench FS II Fast IGBT

General Description

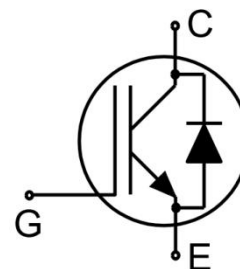
Using NCE's proprietary trench design and advanced FS (Field Stop) second generation technology, the 1200V Trench FSII IGBT offers superior conduction and switching performances, and easy parallel operation;

Features

- Trench FSII Technology Offering
- Very low $V_{CE(sat)}$
- High speed switching
- Positive temperature coefficient in $V_{CE(sat)}$
- Very tight parameter distribution
- High ruggedness, temperature stable behavior

Application

- Inverters
- Motor drives
- Converter



Schematic diagram

Package Marking and Ordering Information

Device	Device Package	Device Marking
NCE50TD120BP	TO-3P	NCE50TD120BP



TO-3P

Absolute Maximum Ratings ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Units
V_{CES}	Collector-Emitter Voltage	1200	V
V_{GES}	Gate- Emitter Voltage	± 30	V
I_C	Collector Current	100	A
	Collector Current @ $T_C = 100^{\circ}\text{C}$	50	A
I_{Cpuls}	Pulsed Collector Current, t_p limited by T_{jmax}	150	A
-	turn off safe operating area, $V_{CE}=1200\text{V}$, $T_j=175^{\circ}\text{C}$	150	A
I_F	Diode Continuous Forward Current @ $T_C = 100^{\circ}\text{C}$	50	A
I_{FM}	Diode Maximum Forward Current	150	A
P_D	Power Dissipation @ $T_C = 25^{\circ}\text{C}$	535	W
	Power Dissipation @ $T_C = 100^{\circ}\text{C}$	268	W
T_J, T_{stg}	Operating Junction and Storage Temperature Range	-55 to +175	$^{\circ}\text{C}$
T_L	Maximum Temperature for Soldering	260	$^{\circ}\text{C}$
t_{sc}	Short circuit withstand time $V_{GE}=15.0\text{V}$, $V_{CC} \leq 600\text{V}$, Allowed number of short circuits<1000Time between short circuits: $\geq 1.0\text{s}$, $T_j \leq 150^{\circ}\text{C}$	10	μs

Thermal Characteristic

Symbol	Parameter	Value	Units
$R_{\theta JC}$	Thermal Resistance, Junction to case for IGBT	0.28	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to case for Diode	0.50	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	°C/W

Electrical Characteristics ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	Value			Units
			Min.	Typ.	Max.	
Static Characteristics						
$V_{(BR)CES}$	Collector-Emitter Breakdown Voltage	$V_{GE}=0V, I_{CE}=1mA$	1200	--	--	V
I_{CES}	Collector-Emitter Leakage Current	$V_{GE}=0V, V_{CE}=1200V$	--	--	200	μA
$I_{GES(F)}$	Gate to Emitter Forward Leakage	$V_{GE}=+30V, V_{CE}=0V$	--	--	200	nA
$I_{GES(R)}$	Gate to Emitter Reverse Leakage	$V_{GE}=-30V, V_{CE}=0V$	--	--	200	nA
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C=50A$ $V_{GE}=15V$	--	1.55	1.80	V
		$T_J=25^\circ\text{C}$ $T_J=175^\circ\text{C}$	--	1.80	--	V
$V_{GE(th)}$	Gate Threshold Voltage	$I_C=1mA, V_{CE}=V_{GE}$	5.0	--	6.5	V
$I_{C(SC)}$	Short circuit collector current Max.1000 short circuits Time between short circuits: $\geq 1.0s$	$V_{GE}=15V, V_{CC} \leq 600V,$ $t_{SC} \leq 10\mu s, T_J \leq 150^\circ\text{C}$	--	290	--	A
Dynamic Characteristics						
C_{ies}	Input Capacitance	$V_{CE}=30V, V_{GE}=0V,$ $f=1MHz$	--	6500	--	pF
C_{oes}	Output Capacitance		--	218	--	
C_{res}	Reverse Transfer Capacitance		--	180	--	
Q_g	Total Gate Charge	$V_{CC}=960V, I_C=50A,$ $V_{GE}=15V$	--	381	--	nC
Q_{ge}	Gate to Emitter Charge		--	46	--	
Q_{gc}	Gate to Collector Charge		--	195	--	
Switching Characteristics						
$t_{d(ON)}$	Turn-on Delay Time	$V_{CE}=600V, I_C=50A,$ $V_{GE}=0/15V, R_g=8\Omega$ Inductive Load	--	19	--	ns
t_r	Rise Time		--	17	--	
$t_{d(OFF)}$	Turn-Off Delay Time		--	170	--	
t_f	Fall Time		--	18	--	
E_{on}	Turn-On Switching Loss		--	2.8	--	mJ
E_{off}	Turn-Off Switching Loss		--	2.0	--	
E_{ts}	Total Switching Loss		--	4.8	--	

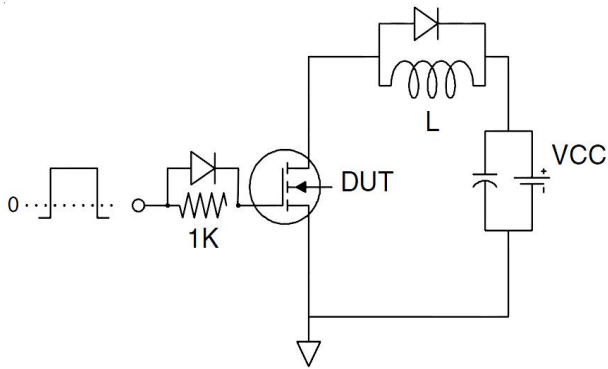
Electrical Characteristics of the Diode($T_c=25^\circ\text{C}$ unless otherwise specified):

Symbol	Parameter	Test Conditions	Rating			Units
			Min.	Typ.	Max.	
V_{FM}	Diode Forward Voltage	$I_F=50A$	--	2.2	2.8	V
T_{rr}	Reverse Recovery Time	$I_F=25A,$ $di/dt=700A/\mu s$	--	150	--	ns
I_{RRM}	Diode Peak Reverse Recovery Current		--	10	--	A
Q_{rr}	Reverse Recovery Charge		--	0.75	--	μC

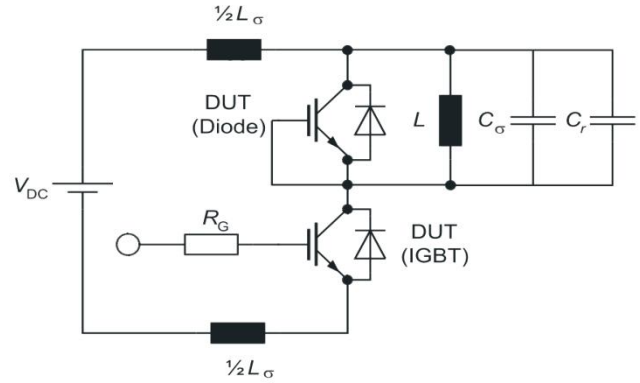
Pulse width $t_p \leq 380\mu s, \delta \leq 2\%$

Test Circuit

1) Gate Charge Test Circuit

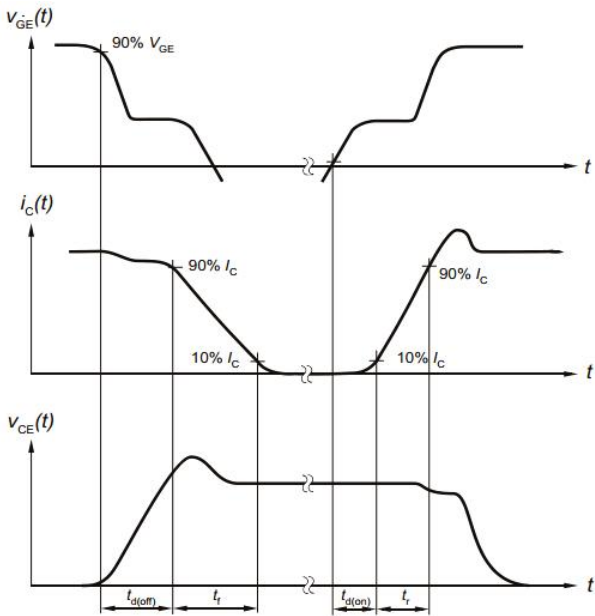


2) Switch Time Test Circuit

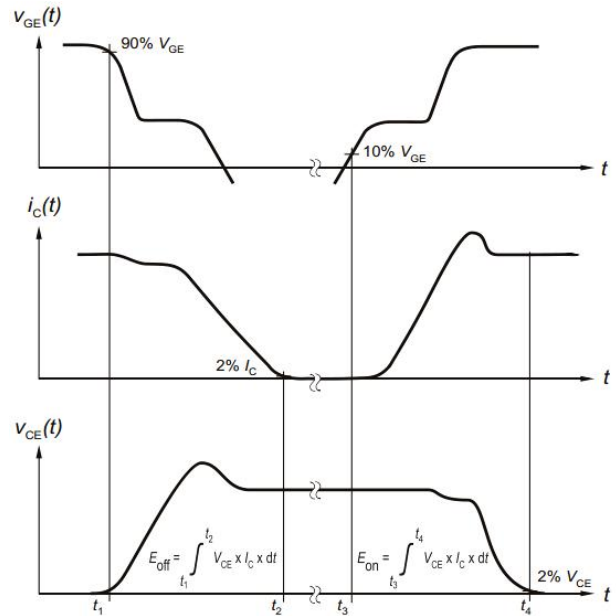


Switching characteristics

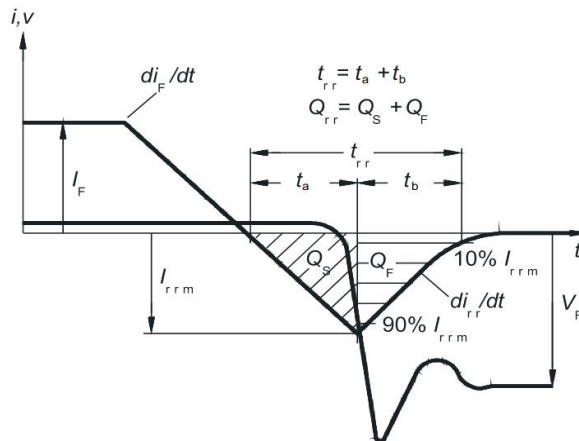
1) Definition of switching times



2) Definition of switching losses



3) Definition of diode switching characteristics



Typical Electrical and Thermal Characteristics

Figure 1 Output Characteristics

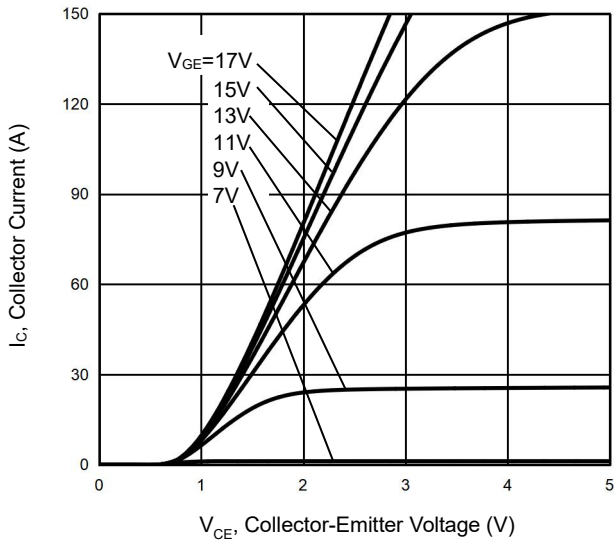


Figure 2 Transfer Characteristics

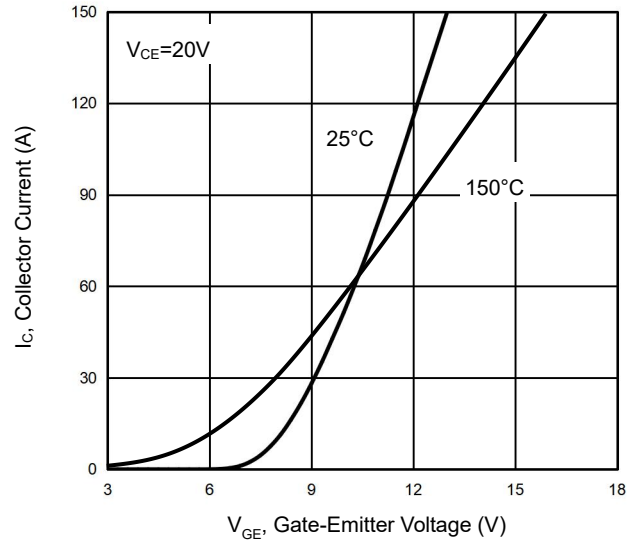


Figure 3 $V_{CE(sat)}$ vs. Case Temperature

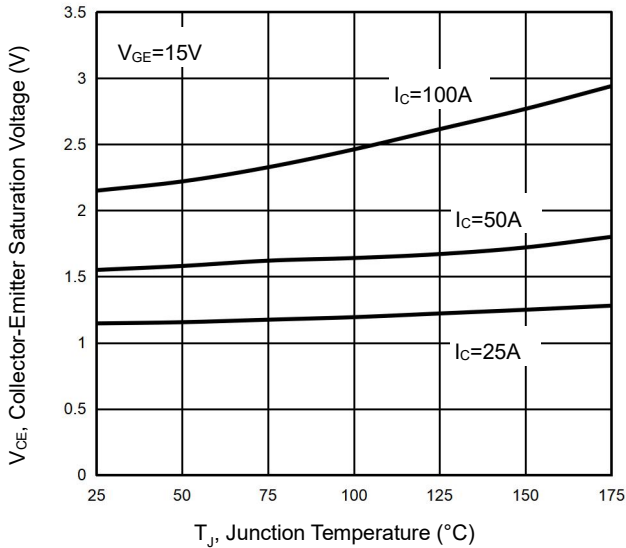


Figure 4 Saturation Voltage vs. V_{GE}

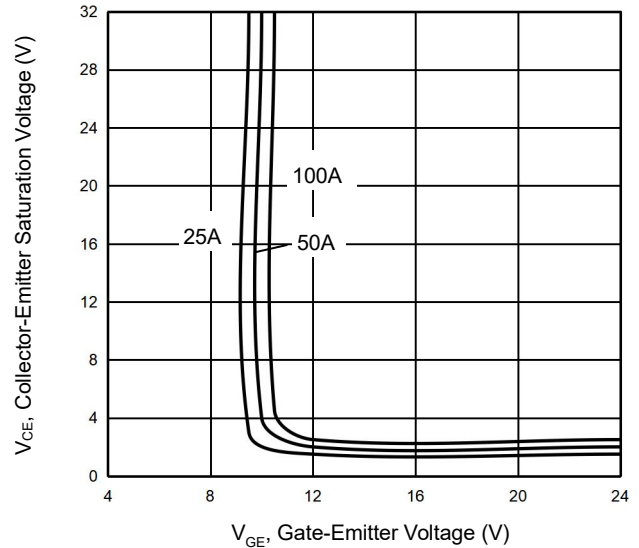


Figure 5 Capacitance Characteristics

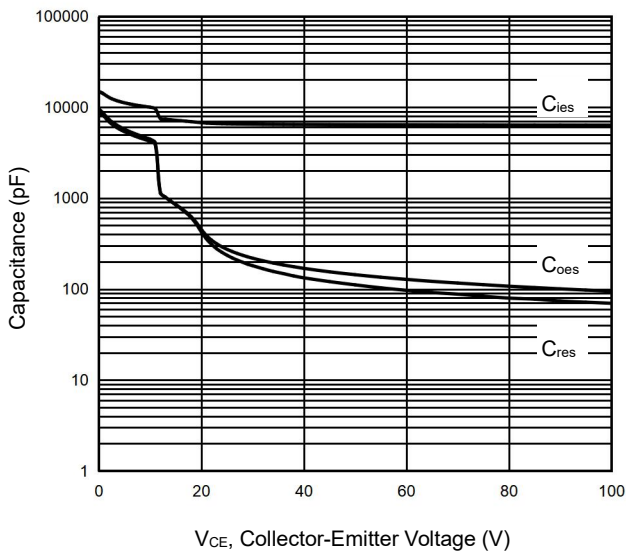
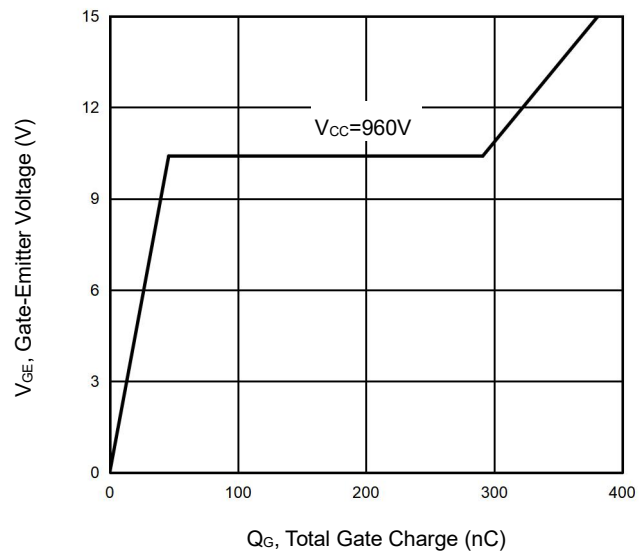


Figure 6 Gate Charge Wave Form



Typical Electrical and Thermal Characteristic

Figure 7 Forward Characteristics

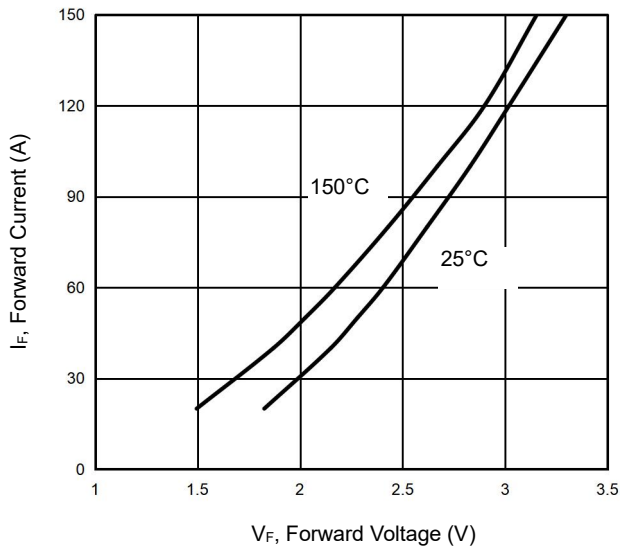


Figure 8 V_F vs. Temperature

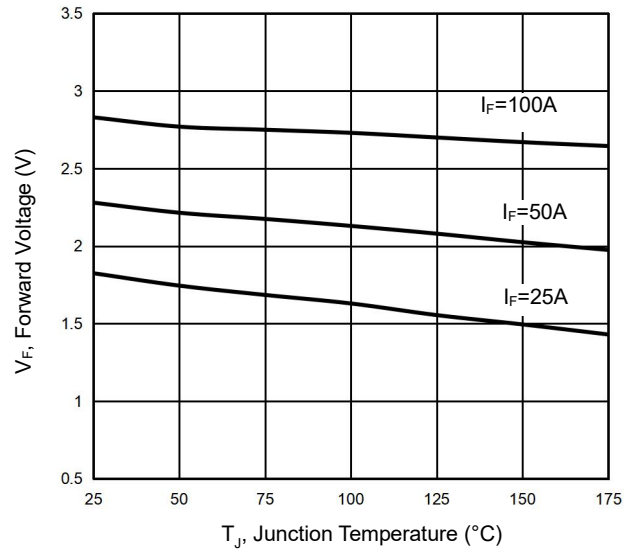


Figure 9 Switching Energy vs. Temperature

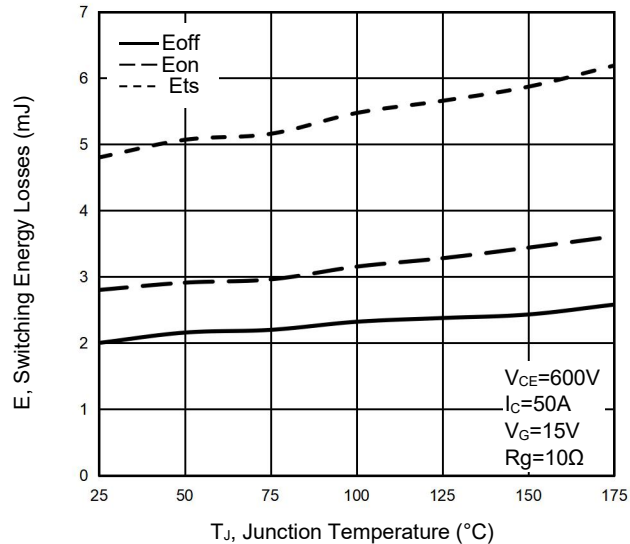


Figure 10 Forward Bias Safe Operating Area

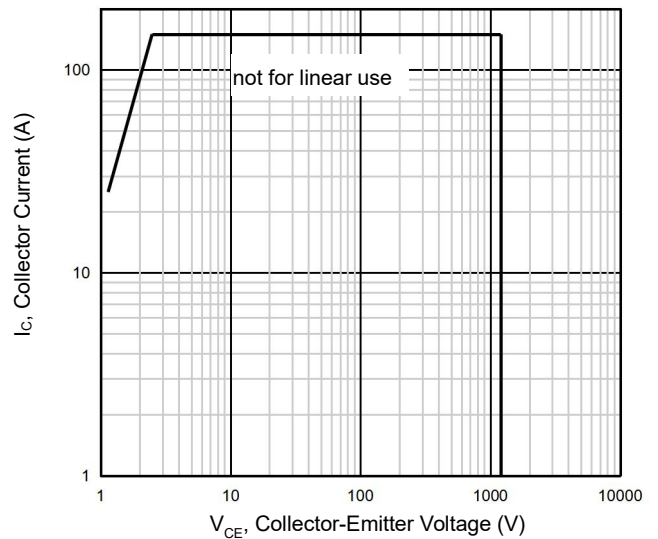


Figure 11 Gate-Emitter Threshold Voltage as a Function of Junction Temperature

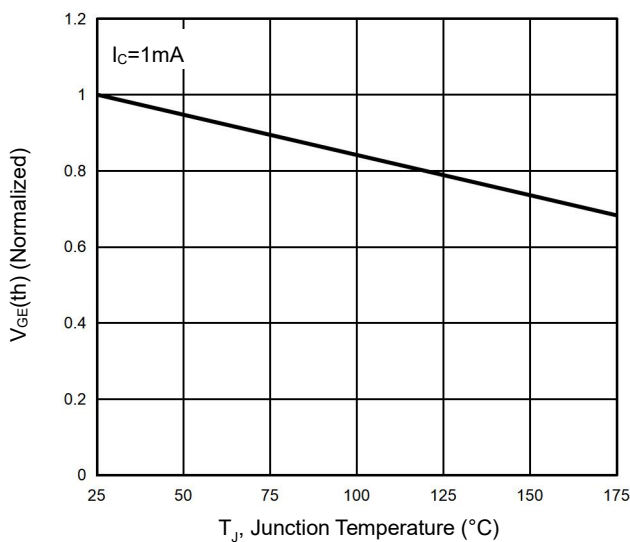
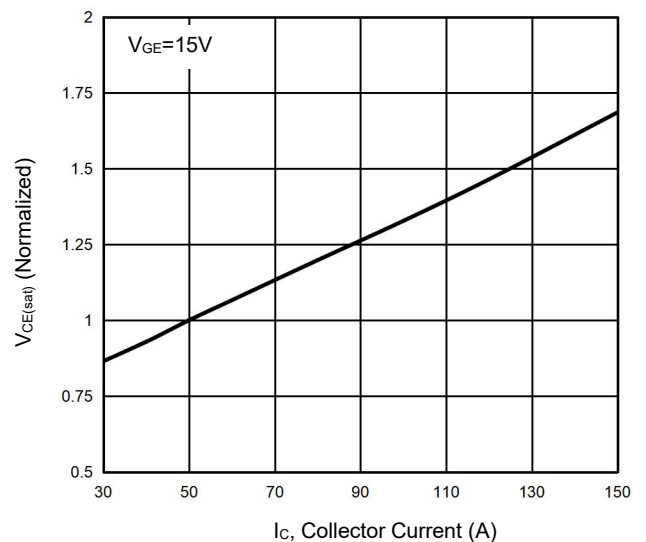


Figure 12 Typical Collector-Emitter Saturation Voltage as a function of Collector Current



Typical Electrical and Thermal Characteristics

Figure 13 Switching Loss vs. R_G

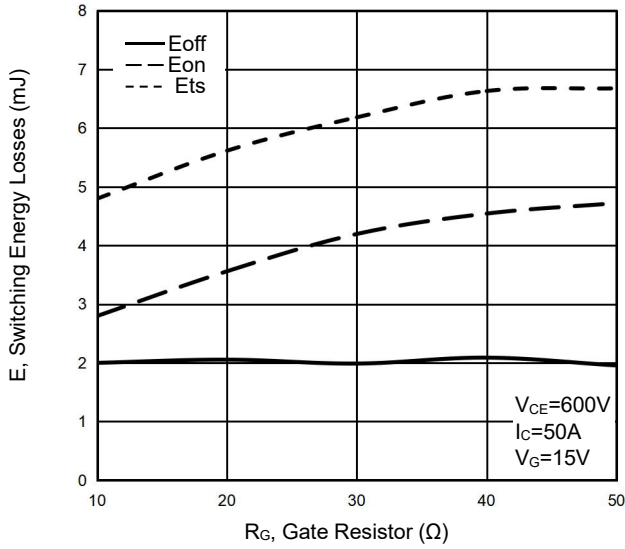


Figure 14 Switching Loss vs. Collector Current

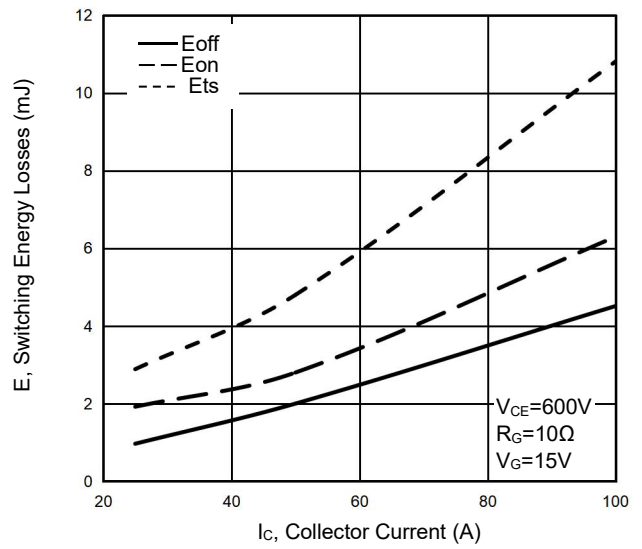


Figure 15 Switching Loss vs. Collector Current

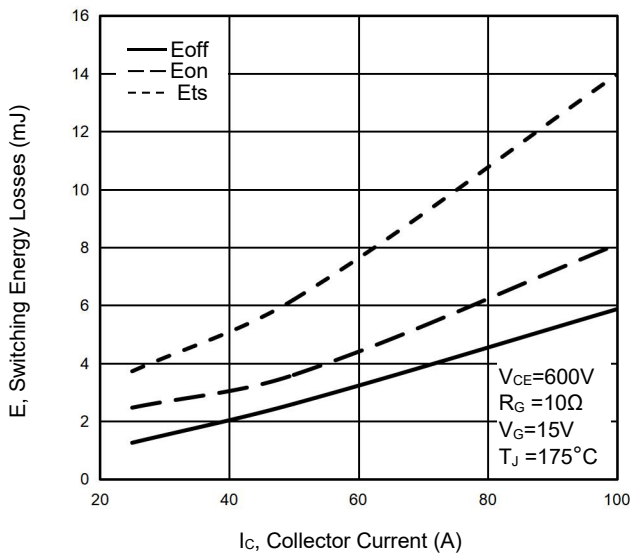


Figure 16 P_{tot} vs. Case Temperature

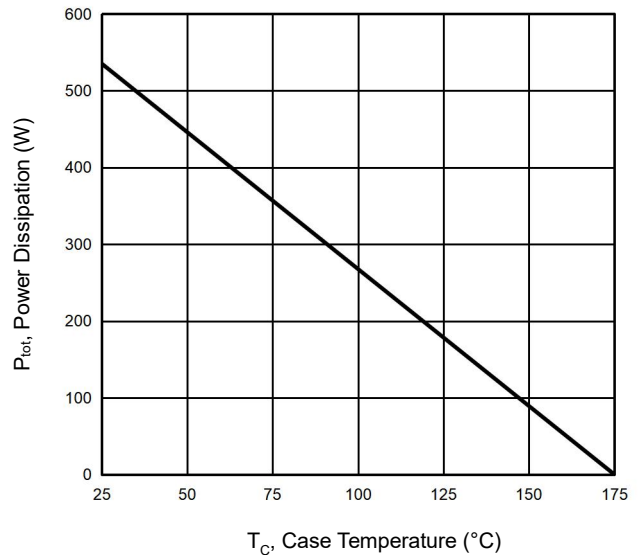


Figure 17 V_{CES} vs. Temperature

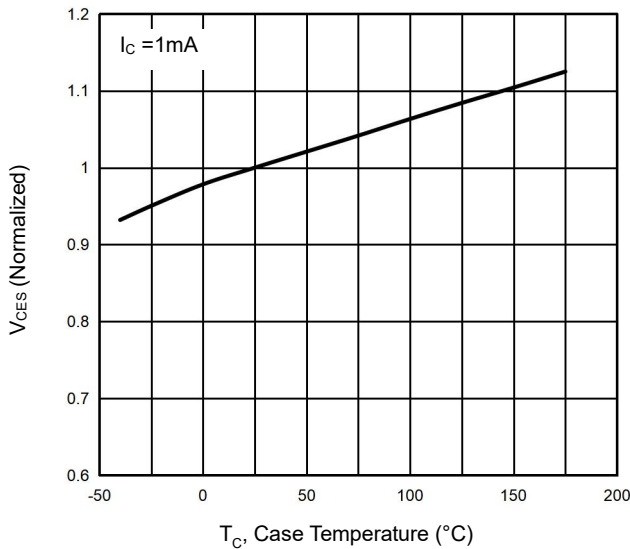
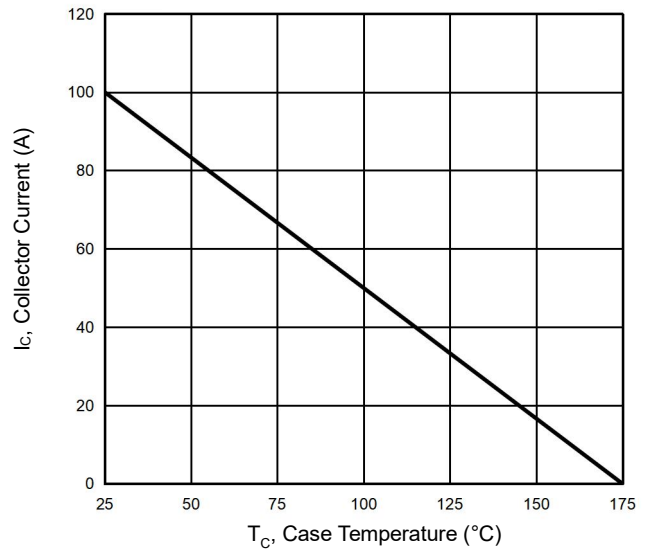
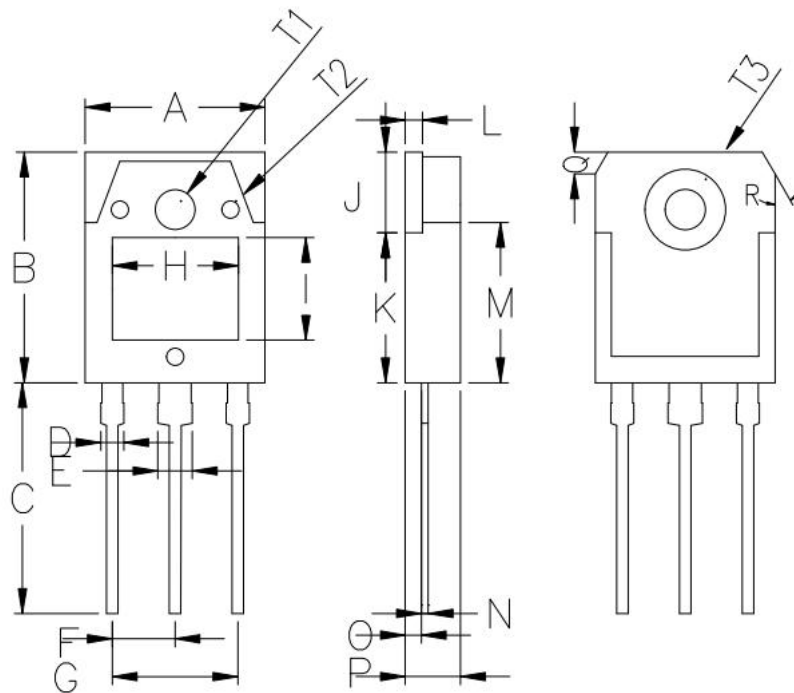


Figure 18 I_C vs. Temperature



TO-3P-S Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	15.50	15.70	0.61	0.62
B	19.70	20.10	0.78	0.79
C	20.10	20.50	0.79	0.81
D	2.00		0.08	
E	3.00		0.12	
F	5.45		0.21	
G	10.90		0.43	
H	10.80	11.00	0.43	0.43
I	8.80	9.00	0.35	0.35
J	6.85	7.15	0.27	0.28
K	12.75	13.05	0.50	0.51
L	1.49	1.51	0.06	0.06
M	13.70	14.00	0.54	0.55
N	0.59	0.61	0.02	0.02
O	1.32	1.48	0.05	0.06
P	4.70	4.90	0.19	0.19
Q	1.90	2.10	0.07	0.08
R	30°		30°	
S	4°		4°	
T1	3.50		0.14	
T2	1.50		0.06	
T3	7.00		0.28	

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