#### NCE N-Channel Enhancement Mode Power MOSFET

#### **Description**

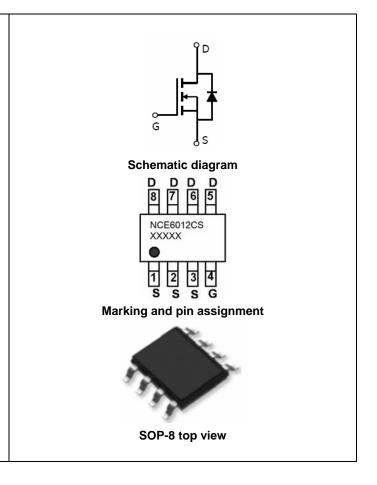
The NCE6012CS uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

#### **General Features**

- $V_{DS} = 60V, I_D = 12A$  $R_{DS(ON)} < 11mΩ @ V_{GS} = 10V$  (Typ:9mΩ)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Low gate to drain charge to reduce switching losses

#### **Application**

- Power switching application
- Load switch



#### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE6012CS	NCE6012CS	SOP-8	Ø330mm	12mm	4000 units

## Absolute Maximum Ratings (T<sub>C</sub>=25 ℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	60	V
Gate-Source Voltage	V <sub>G</sub> S	±20	V
Drain Current-Continuous	I <sub>D</sub>	12	А
Drain Current-Continuous(T <sub>C</sub> =100 °C)	I <sub>D</sub> (100℃)	8.5	А
Pulsed Drain Current	I <sub>DM</sub>	50	А
Maximum Power Dissipation	P <sub>D</sub>	3	W
Operating Junction and Storage Temperature Range	$T_{J}$ , $T_{STG}$	-55 To 150	$^{\circ}$

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	42	°C/W

# NCE6012CS

# Electrical Characteristics (TC=25°Cunless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250μA	60		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	$V_{GS}$ =10V, $I_D$ =12A	-	9	11	mΩ
Forward Transconductance	<b>g</b> FS	V <sub>DS</sub> =5V,I <sub>D</sub> =12A	40	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C <sub>lss</sub>	\/ -20\/\/ -0\/	-	3080	-	PF
Output Capacitance	Coss	$V_{DS}$ =30V, $V_{GS}$ =0V, F=1.0MHz	-	272	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	Γ-1.UIVIΠZ	-	195	-	PF
Switching Characteristics (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =30V, $R_L$ =1 $\Omega$	-	6.5	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10 $V$ , $R_{GEN}$ =3 $\Omega$	-	36	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	13	-	nS
Total Gate Charge	$Q_g$	\/ -20\/   -424	-	55.5	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =30V, $I_D$ =12A, $V_{GS}$ =10V	-	14.5	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =10V	-	16.4	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =12A	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	12	Α
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C, IF=12A	-	30	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	42	-	nC

#### Notes:

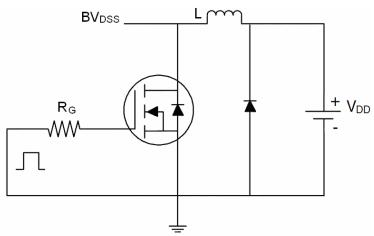
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production



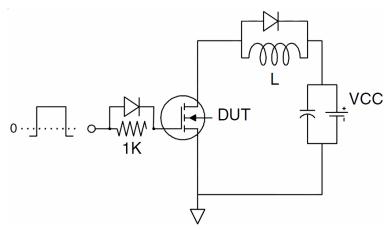
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#### **Test Circuit**

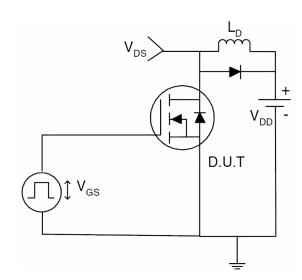
## 1) E<sub>AS</sub> test Circuit



# 2) Gate charge test Circuit

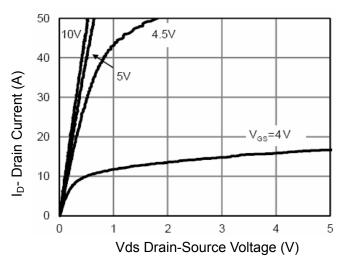


# 3) Switch Time Test Circuit

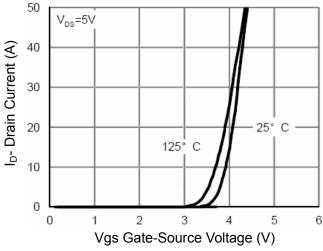




### **Typical Electrical and Thermal Characteristics (Curves)**



**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

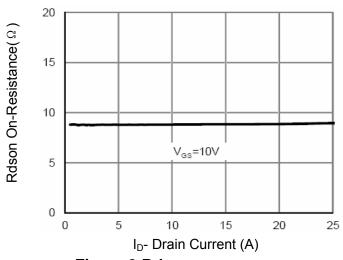


Figure 3 Rdson- Drain Current

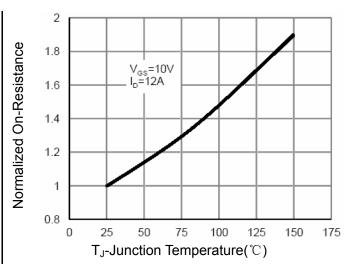


Figure 4 Rdson-JunctionTemperature

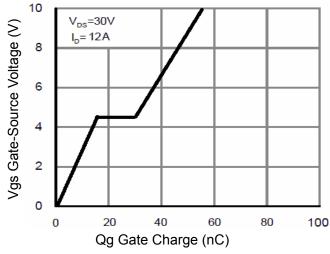


Figure 5 Gate Charge

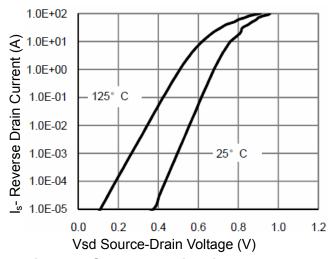
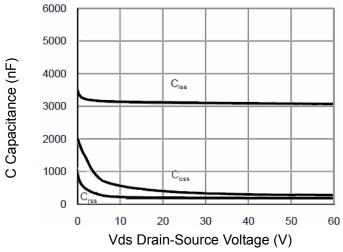


Figure 6 Source- Drain Diode Forward



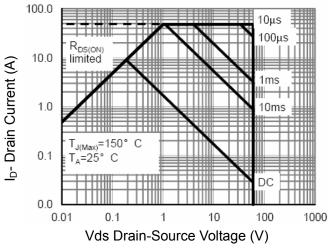




16
14
12
10
8
6
4
2
0
25 50 75 100 125 150
T<sub>J</sub>-Junction Temperature(°C)

Figure 7 Capacitance vs Vds

**Figure 9 Current De-rating** 



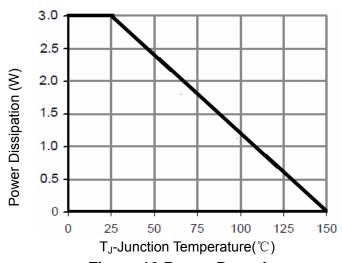
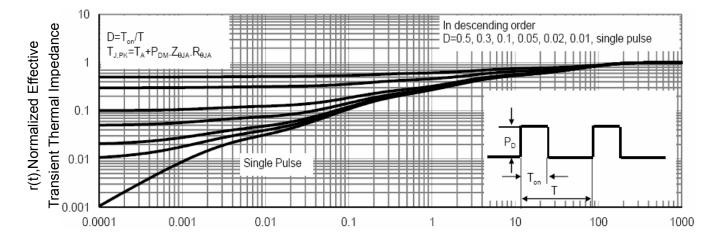


Figure 8 Safe Operation Area

Figure 10 Power De-rating



ID- Drain Current (A)

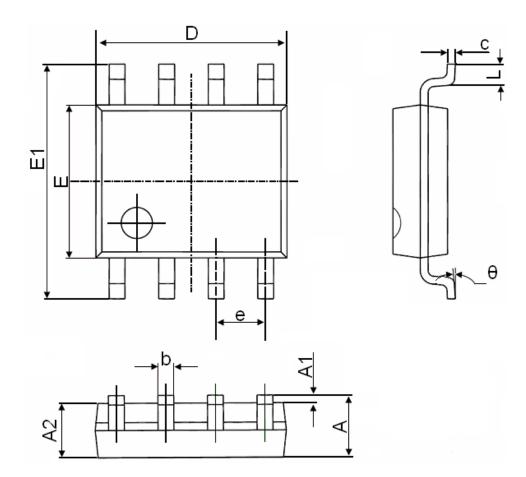
Square Wave Pluse Duration(sec)

**Figure 11 Normalized Maximum Transient Thermal Impedance** 



# NCE6012CS

# **SOP-8 Package Information**



Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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# NCE6012CS

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