

### NCE P-Channel Enhancement Mode Power MOSFET

### **Description**

The NCE60P16AQ uses advanced trench technology to provide excellent  $R_{\text{DS(ON)}}$ , This device is suitable for use as a load switch or power management.

### **Application**

- Power management
- Load switch

100% UIS TESTED! 100% ΔVds TESTED!

#### **General Features**

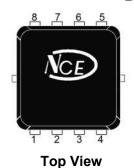
•  $V_{DS} = -60V, I_{D} = -16A$ 

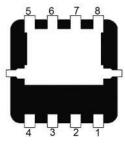
 $R_{DS(ON)}\!<\!65m\Omega$  @  $V_{GS}\!=\!-10V$ 

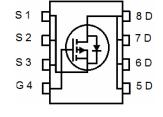
 $R_{DS(ON)}$  <85m $\Omega$  @  $V_{GS}$ =-4.5V

- High power and current handing capability
- Lead free product is acquired
- Surface mount package

#### **DFN 3.3X3.3**







**Bottom View** 

**Schematic Diagram** 

### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE60P16AQ	NCE60P16AQ	DFN3.3X3.3-8L	-	-	-

### Absolute Maximum Ratings (T<sub>C</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	-60	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	V	
Drain Current-Continuous	I <sub>D</sub>	-16	А	
Pulsed Drain Current	I <sub>DM</sub>	-64	Α	
Maximum Power Dissipation	P <sub>D</sub>	30	W	
Operating Junction and Storage Temperature Range	$T_{J}, T_{STG}$	-55 To 150	$^{\circ}$	

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{ hetaJA}$	4.2	°C/W



# Electrical Characteristics (T<sub>C</sub>=25 ℃ unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			•			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =-250μA	-60	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-60V,V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)			•			
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$ , $I_{D}=-250\mu A$	-1.0	-1.5	-2.0	V
Drain Source On State Resistance	-	V <sub>GS</sub> =-10V, I <sub>D</sub> =-8A	-	55	65	mΩ
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-8A	-	70	85	mΩ
Forward Transconductance	<b>g</b> FS	$V_{DS}$ =-5 $V$ , $I_{D}$ =-8 $A$	-	10	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C <sub>lss</sub>	\\ 20\\\\ 0\\	-	1153	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =-30V, $V_{GS}$ =0V, F=1.0MHz	-	93.7	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.UIVIHZ	-	77.7	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	8	-	nS
Turn-on Rise Time	t <sub>r</sub>	$V_{DD}$ =-30V, $R_L$ =6 $\Omega$ ,	-	5	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =-10 $V$ , $R_{G}$ =3 $\Omega$	-	32	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	8	-	nS
Total Gate Charge	$Q_g$	V 00 L 04	-	15.8	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =-30, $I_{D}$ =-8A, $V_{GS}$ =-10V	-	2.7	-	nC
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =-10V	-	3.5	-	nC
Drain-Source Diode Characteristics			•			
Diode Forward Voltage (Note 3)	$V_{SD}$	V <sub>GS</sub> =0V,I <sub>S</sub> =-8A	-		-1.2	V
Diode Forward Current (Note 2)	Is		-	-	-16	Α
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> =- 8A	-	27		nS
Reverse Recovery Charge	Qrr	$di/dt = -100A/\mu s^{(Note3)}$	-	32		nC

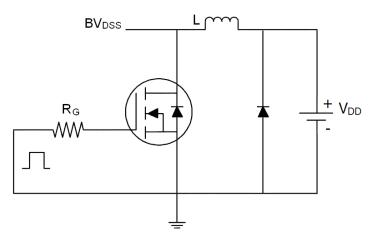
#### Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board,  $t \le 10$  sec.
- 3. Pulse Test: Pulse Width  $\leq$  300 $\mu$ s, Duty Cycle  $\leq$  2%.
- 4. Guaranteed by design, not subject to production

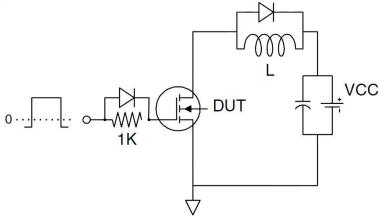


### **Test Circuit**

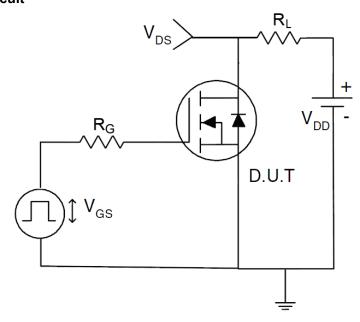
# 1) E<sub>AS</sub> test Circuit



# 2) Gate charge test Circuit



### 3) Switch Time Test Circuit





### **Typical Electrical and Thermal Characteristics (Curves)**

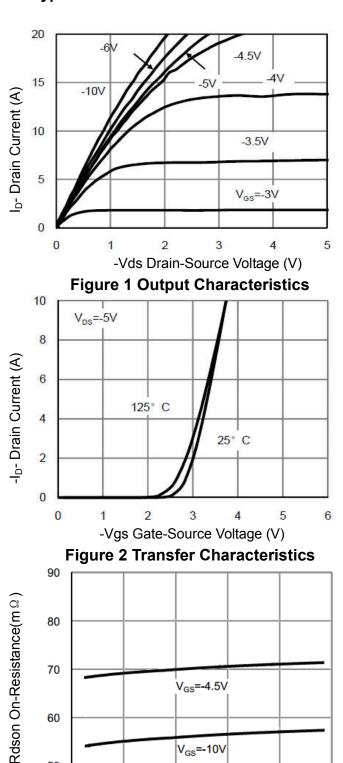


Figure 3 Rdson- Drain Current

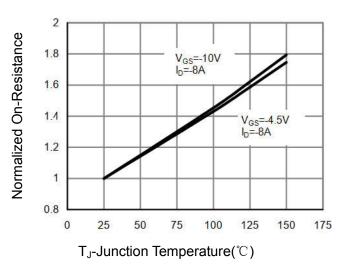
- I<sub>D</sub>- Drain Current (A)

8

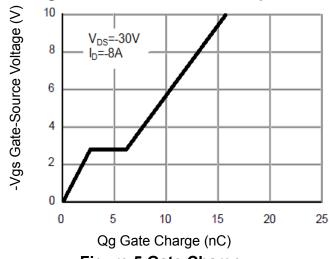
10

50

0



**Figure 4 Rdson-Junction Temperature** 



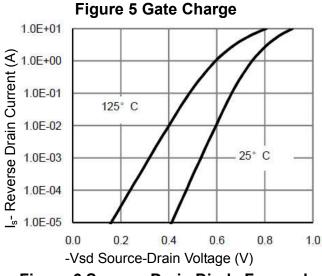


Figure 6 Source- Drain Diode Forward



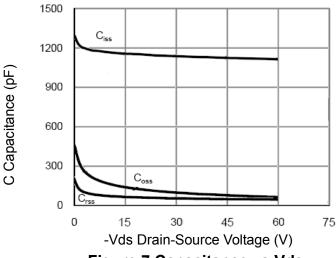


Figure 7 Capacitance vs Vds

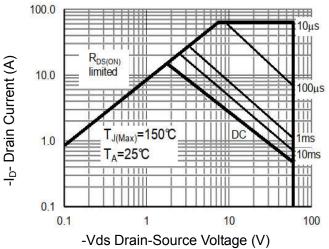


Figure 8 Safe Operation Area

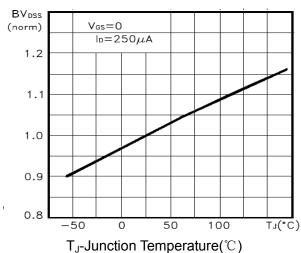


Figure 9 BV<sub>DSS</sub> vs Junction Temperature

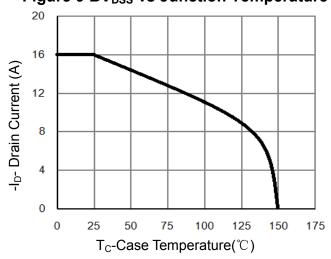
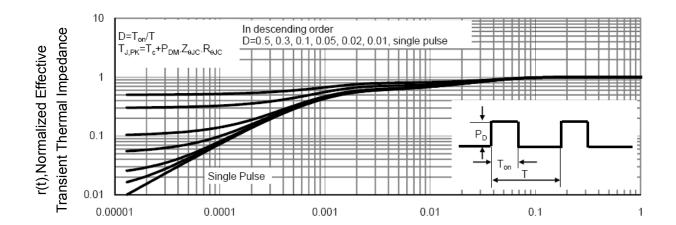


Figure 10 ID Current De-rating

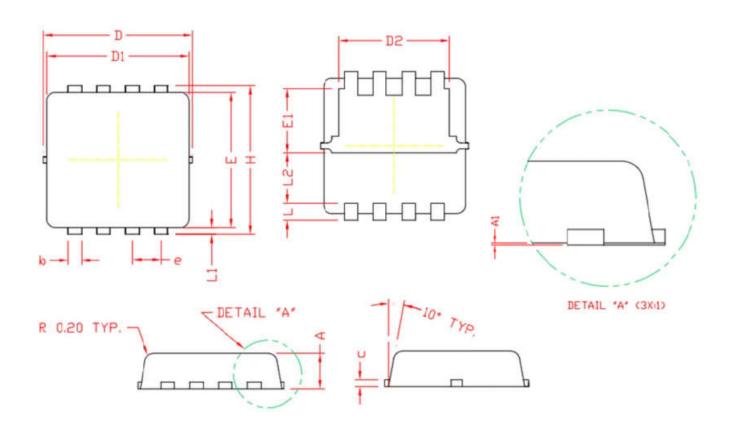


Square Wave Pluse Duration(sec)

Figure 11 Normalized Maximum Transient Thermal Impedance



# **DFN3.3X3.3-8L Package Information**



# COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	0.70	0.80	0.90
A1	0.00	0.03	0.05
b	0.24	0.30	0.35
С	0.10	0.15	0.20
D	3. 25	3.32	3.40
D1	3.05	3. 15	3.25
D2	2.40	2.50	2.60
E	3.00	3.10	3.20
E1	1. 35	1.45	1.55
е	0	.65 BSC	•
Н	3. 20	3.30	3.40
L	0.30	0.40	0.50
L1	0.10	0.15	0.20
L2	1.13 REF.		

#### http://www.ncepower.com

# NCE60P16AQ

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