NCE N-Channel Super Trench II Power MOSFET

Description

The NCEP18N10AR uses **Super Trench II** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification

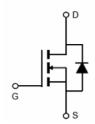
General Features

- V_{DS} =100V,I_D =9.5A
 - $R_{DS(ON)}$ =19.2m Ω (typical) @ V_{GS} =10V $R_{DS(ON)}$ =23.0m Ω (typical) @ V_{GS} =4.5V
- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance R_{DS(on)}
- 150 °C operating temperature
- Pb-free lead plating

100% UIS TESTED! 100% ΔVds TESTED!

SOT223





Schematic Diagram

Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP18N10AR	NCEP18N10AR	SOT223-3L	Ø330mm	12mm	2500 units

Absolute Maximum Ratings (T_C=25 ℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	100	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	9.5	Α
Drain Current-Continuous(T _C =100℃)	I _D (100℃)	6.6	Α
Pulsed Drain Current (Note 1)	I _{DM}	38	Α
Maximum Power Dissipation	P _D	2.5	W
Single pulse avalanche energy (Note 5)	E _{AS}	115	mJ
Operating Junction and Storage Temperature Range	T_{J}, T_{STG}	-55 To 150	$^{\circ}$

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{θJA}	50	°C/W	
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	100		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =100V,V _{GS} =0V	-	-	1	μΑ
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	1.2	1.7	2.2	V
Dunin Course On State Besistance	_	V _{GS} =10V, I _D =5A	-	19.2	20	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =5A	-	23.0	25	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =5A	20	-	-	S
Dynamic Characteristics (Note4)						
Input Capacitance	C _{lss}	\/ 5 0\/\/ 0\/	-	1719.5	-	PF
Output Capacitance	Coss	V_{DS} =50V, V_{GS} =0V, F=1.0MHz	-	147.4	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.UIVIHZ	-	16	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	14	-	nS
Turn-on Rise Time	t _r	V_{DD} =50 V , I_{D} =5 A	-	16	-	nS
Turn-Off Delay Time	t _{d(off)}	V_{GS} =10 V , R_{G} =3 Ω	-	28	-	nS
Turn-Off Fall Time	t _f		-	8	-	nS
Total Gate Charge	Qg	\/ F0\/\	-	37.6	-	nC
Gate-Source Charge	Q _{gs}	V_{DS} =50 V , I_{D} =5 A ,	-	6.5		nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	9.5		nC
Drain-Source Diode Characteristics			1			
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =5A	-		1.2	V
Diode Forward Current (Note 2)	Is		-	-	9.5	Α
Reverse Recovery Time	t _{rr}	$T_J = 25^{\circ}C, I_F = 5A$	-	43	-	nS
Reverse Recovery Charge	Qrr	$di/dt = 100A/\mu s^{(Note3)}$	-	90	-	nC

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. The value of $R_{\theta JA}$ is measured with the device mounted on 1in2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25° C. The Power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150° C. The value in any given application depends on the user's specific board design. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JC}$ and case to ambient.
- 3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.
- 4. Guaranteed by design, not subject to production
- 5. EAS condition : Tj=25 $^{\circ}\text{C}$,V $_{\text{DD}}$ =50 V,V $_{\text{G}}$ =10 V,L=0.5 mH,Rg=25 Ω



Typical Electrical and Thermal Characteristics

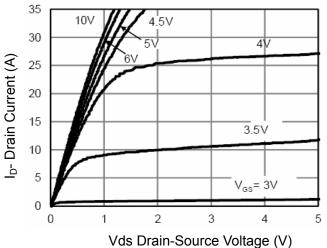


Figure 1 Output Characteristics

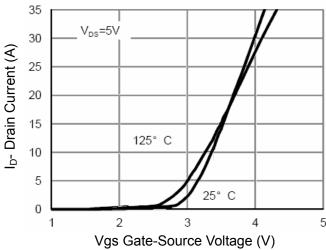


Figure 2 Transfer Characteristics

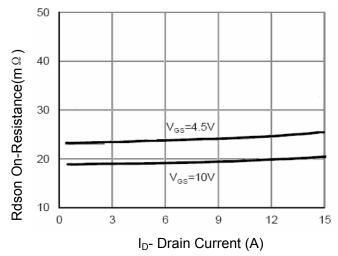


Figure 3 Rdson- Drain Current

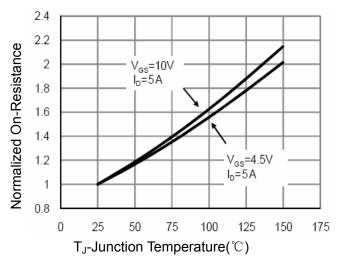


Figure 4 Rdson-Junction Temperature

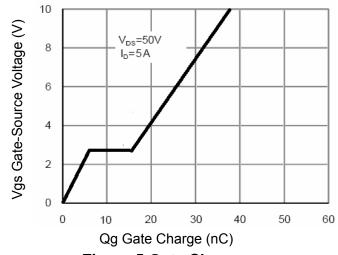


Figure 5 Gate Charge

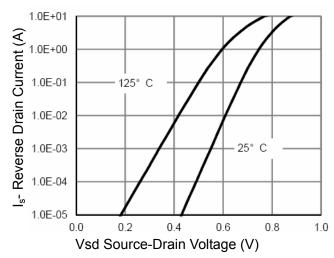


Figure 6 Source- Drain Diode Forward



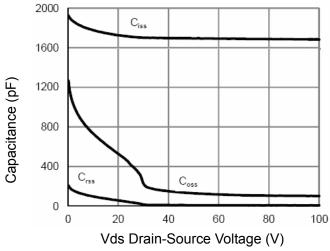


Figure 7 Capacitance vs Vds

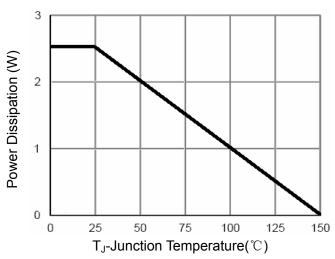


Figure 9 Power De-rating

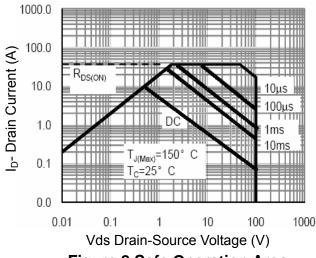


Figure 8 Safe Operation Area

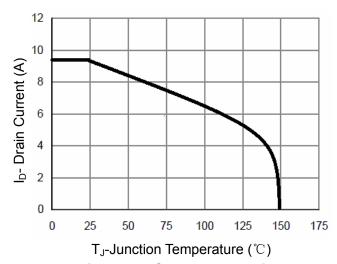


Figure 10 Current De-rating

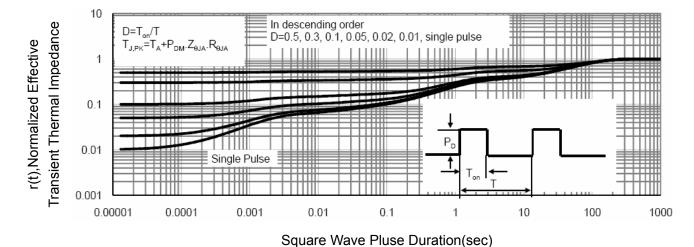
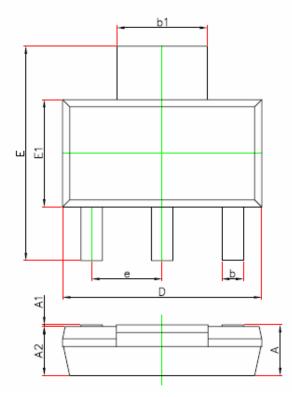
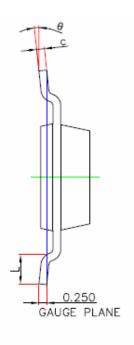


Figure 11 Normalized Maximum Transient Thermal Impedance



SOT-223 Package Information





Symbol	Dimensions In	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α		1.800		0.071	
A1	0.020	0.100	0.001	0.004	
A2	1.500	1.700	0.059	0.067	
b	0.660	0.840	0.026	0.033	
b1	2.900	3.100	0.114	0.122	
С	0.230	0.350	0.009	0.014	
D	6.300	6.700	0.248	0.264	
E	6.700	7.300	0.264	0.287	
E1	3.300	3.700	0.130	0.146	
е	2.300(BSC)		0.091(BSC)		
L	0.750		0.030		
θ	0°	10°	0°	10°	

Notes

- 1. All dimensions are in millimeters.
- 2. Tolerance ±0.10mm (4 mil) unless otherwise specified
- 3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 5 mils.
- 4. Dimension L is measured in gauge plane.
- 5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.

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NCEP18N10AR

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