


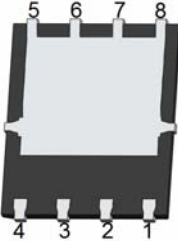
NCE P-Channel Super Trench Power MOSFET

| | |
|--|--|
| <p>Description</p> <p>The NCEP30PT16G uses Super Trench technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g. This device is ideal for high-frequency switching and synchronous rectification.</p> <p>Application</p> <ul style="list-style-type: none"> ● DC/DC Converter ● Ideal for high-frequency switching and synchronous rectification | <p>General Features</p> <ul style="list-style-type: none"> ● $V_{DS} = -30V, I_D = -160A$ $R_{DS(ON)} = 2.3m\Omega$ (typical) @ $V_{GS} = -10V$ $R_{DS(ON)} = 3.3m\Omega$ (typical) @ $V_{GS} = -4.5V$ ● Excellent gate charge x $R_{DS(on)}$ product(FOM) ● Very low on-resistance $R_{DS(on)}$ ● 150 °C operating temperature ● Pb-free lead plating ● 100% UIS tested <p style="text-align: center;">100% UIS TESTED! 100% ΔV_{ds} TESTED!</p> |
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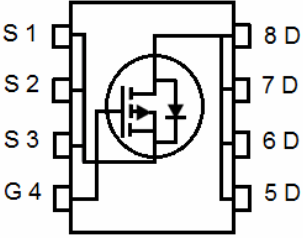
DFN 5X6



Top View



Bottom View



Schematic Diagram

Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity |
|----------------|-------------|----------------|-----------|------------|----------|
| NCEP30PT16G | NCEP30PT16G | DFN5X6-8L | - | - | - |

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|---|--------------------|------------|---------------|
| Drain-Source Voltage | V_{DS} | -30 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous | I_D | -160 | A |
| Drain Current-Continuous($T_C = 100^\circ C$) | $I_D(100^\circ C)$ | -112 | A |
| Pulsed Drain Current | I_{DM} | -640 | A |
| Maximum Power Dissipation | P_D | 150 | W |
| Derating factor | | 1.2 | W/ $^\circ C$ |
| Single pulse avalanche energy ^(Note 5) | E_{AS} | 1076 | mJ |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 150 | $^\circ C$ |

Thermal Characteristic

| | | | |
|--|-----------------|------|--------------|
| Thermal Resistance, Junction-to-Case ^(Note 2) | $R_{\theta JC}$ | 0.83 | $^\circ C/W$ |
|--|-----------------|------|--------------|

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise noted)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|--------------|---|------|-------|-----------|------------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS}=0V, I_D=-250\mu A$ | -30 | | - | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS}=-30V, V_{GS}=0V$ | - | - | 1 | μA |
| Gate-Body Leakage Current | I_{GSS} | $V_{GS}=\pm 20V, V_{DS}=0V$ | - | - | ± 100 | nA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=-250\mu A$ | -1.0 | -1.5 | -2.2 | V |
| Drain-Source On-State Resistance | $R_{DS(on)}$ | $V_{GS}=-10V, I_D=-80A$ | - | 2.3 | 2.7 | m Ω |
| | | $V_{GS}=-4.5V, I_D=-80A$ | - | 3.3 | 4.0 | m Ω |
| Forward Transconductance | g_{FS} | $V_{DS}=-5V, I_D=-80A$ | - | 30 | - | S |
| Dynamic Characteristics (Note 4) | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS}=-15V, V_{GS}=0V,$ $F=1.0MHz$ | - | 7962 | - | PF |
| Output Capacitance | C_{oss} | | - | 2380 | - | PF |
| Reverse Transfer Capacitance | C_{rss} | | - | 216 | - | PF |
| Switching Characteristics (Note 4) | | | | | | |
| Turn-on Delay Time | $t_{d(on)}$ | $V_{DD}=-15V, I_D=-80A$ $V_{GS}=-10V, R_G=1.6\Omega$ | - | 18 | - | nS |
| Turn-on Rise Time | t_r | | - | 13 | - | nS |
| Turn-Off Delay Time | $t_{d(off)}$ | | - | 90 | - | nS |
| Turn-Off Fall Time | t_f | | - | 25 | - | nS |
| Total Gate Charge | Q_g | $V_{DS}=-15V, I_D=-80A,$ $V_{GS}=-10V$ | - | 106.5 | - | nC |
| Gate-Source Charge | Q_{gs} | | - | 15.7 | - | nC |
| Gate-Drain Charge | Q_{gd} | | - | 16.9 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage (Note 3) | V_{SD} | $V_{GS}=0V, I_S=-80A$ | - | | -1.2 | V |
| Diode Forward Current (Note 2) | I_S | | - | - | -160 | A |
| Reverse Recovery Time | t_{rr} | $T_J = 25^\circ\text{C}, I_F = -80A$ $di/dt = 100A/\mu s$ (Note 3) | - | | 35 | nS |
| Reverse Recovery Charge | Q_{rr} | | - | | 85 | nC |

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition : $T_J=25^\circ\text{C}, V_{DD}=-15V, V_G=-10V, L=0.5mH, R_g=25\Omega$

Typical Electrical and Thermal Characteristics

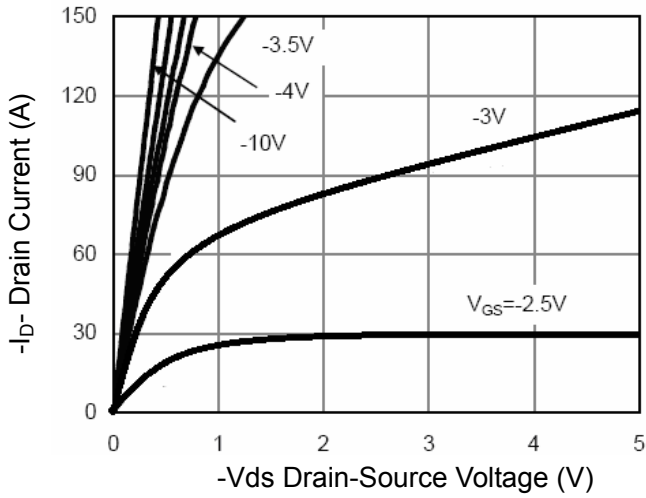


Figure 1 Output Characteristics

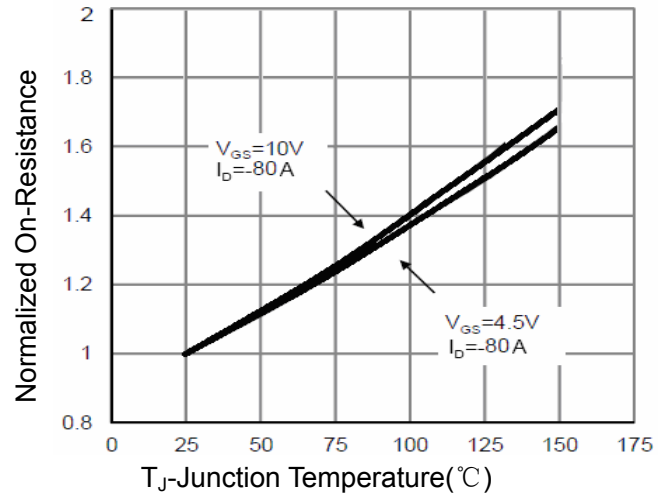


Figure 4 Rdson-Junction Temperature

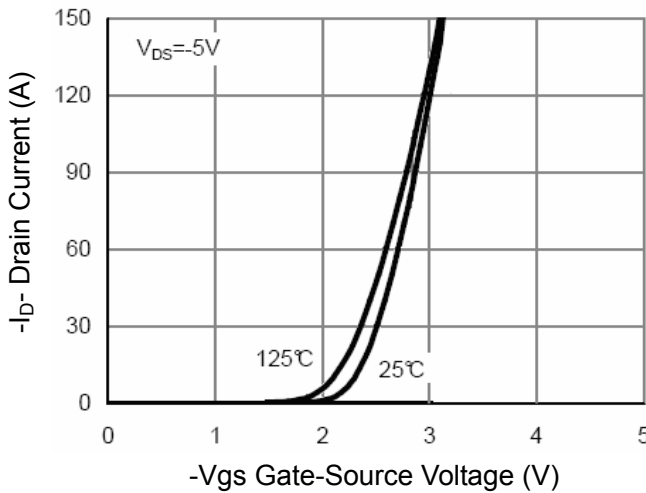


Figure 2 Transfer Characteristics

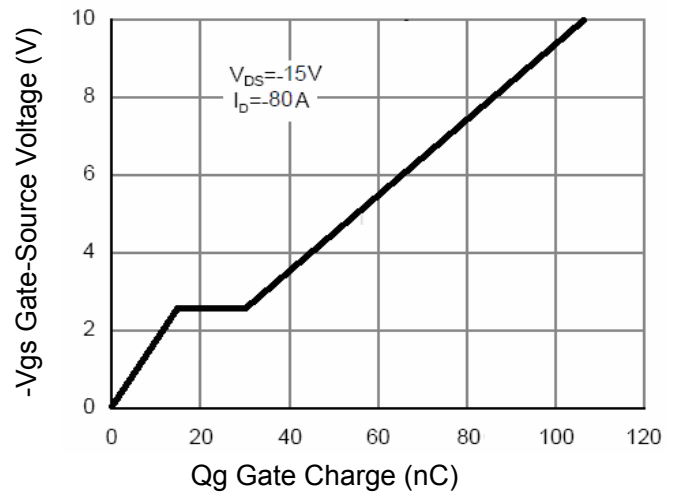


Figure 5 Gate Charge

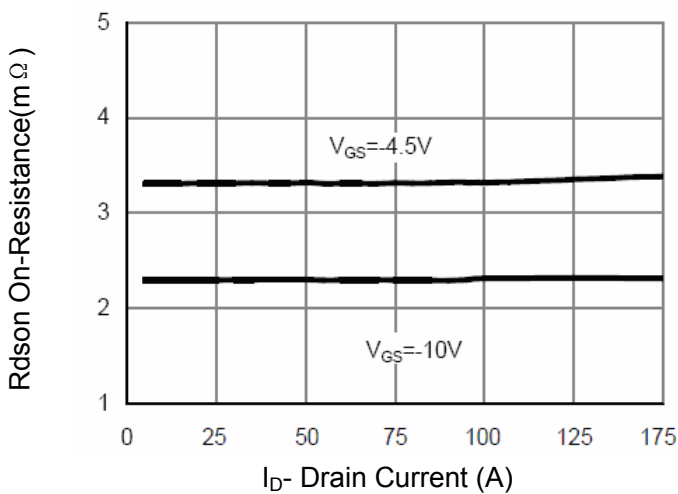


Figure 3 Rdson- Drain Current

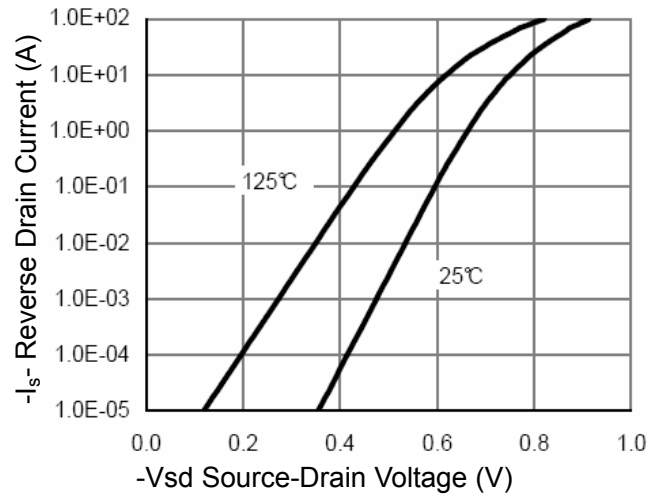


Figure 6 Source- Drain Diode Forward

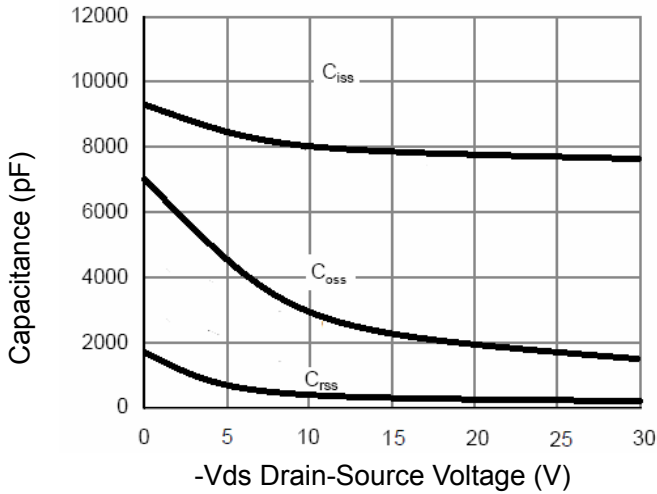


Figure 7 Capacitance vs Vds

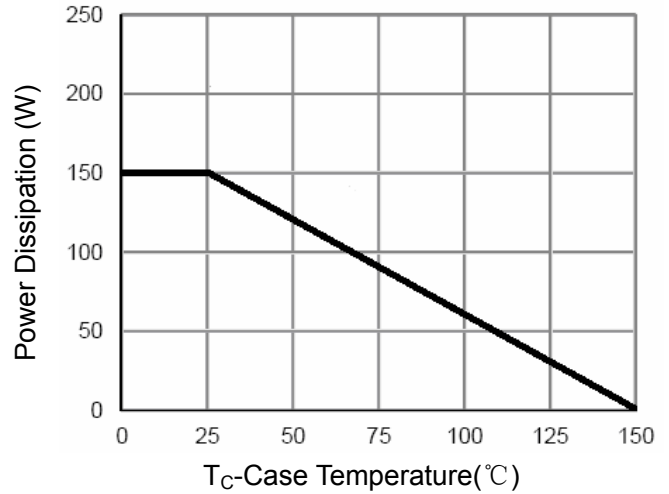


Figure 9 Power De-rating

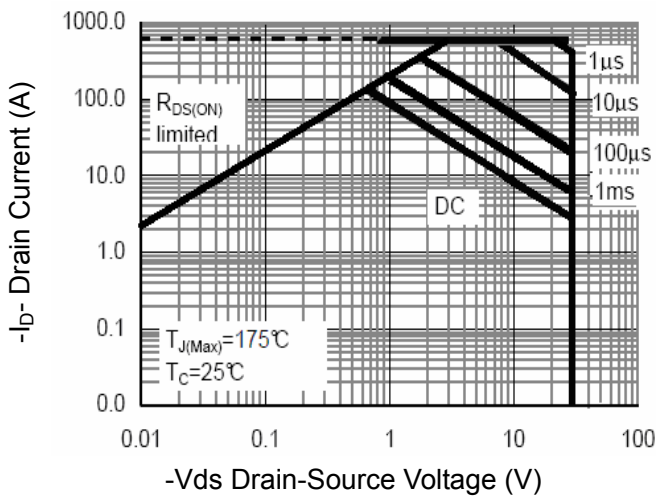


Figure 8 Safe Operation Area

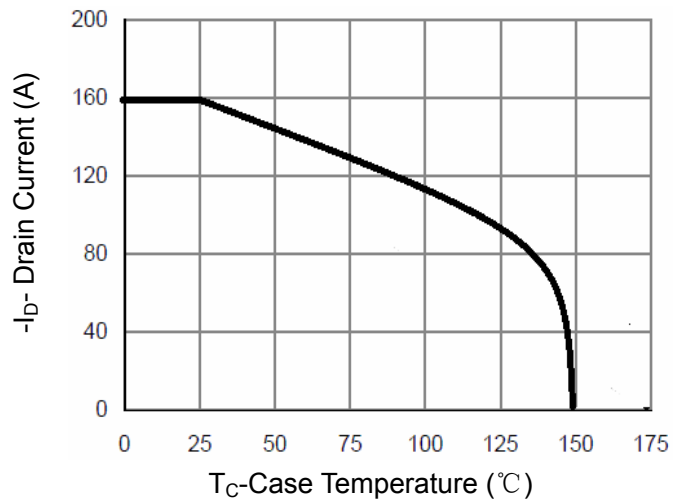


Figure 10 Current De-rating

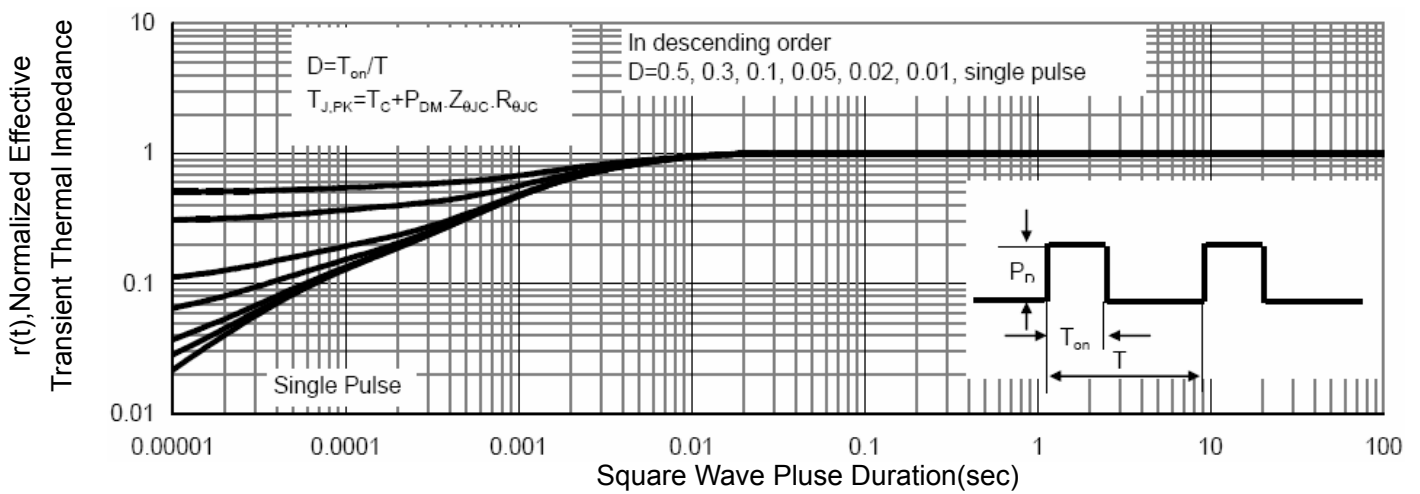
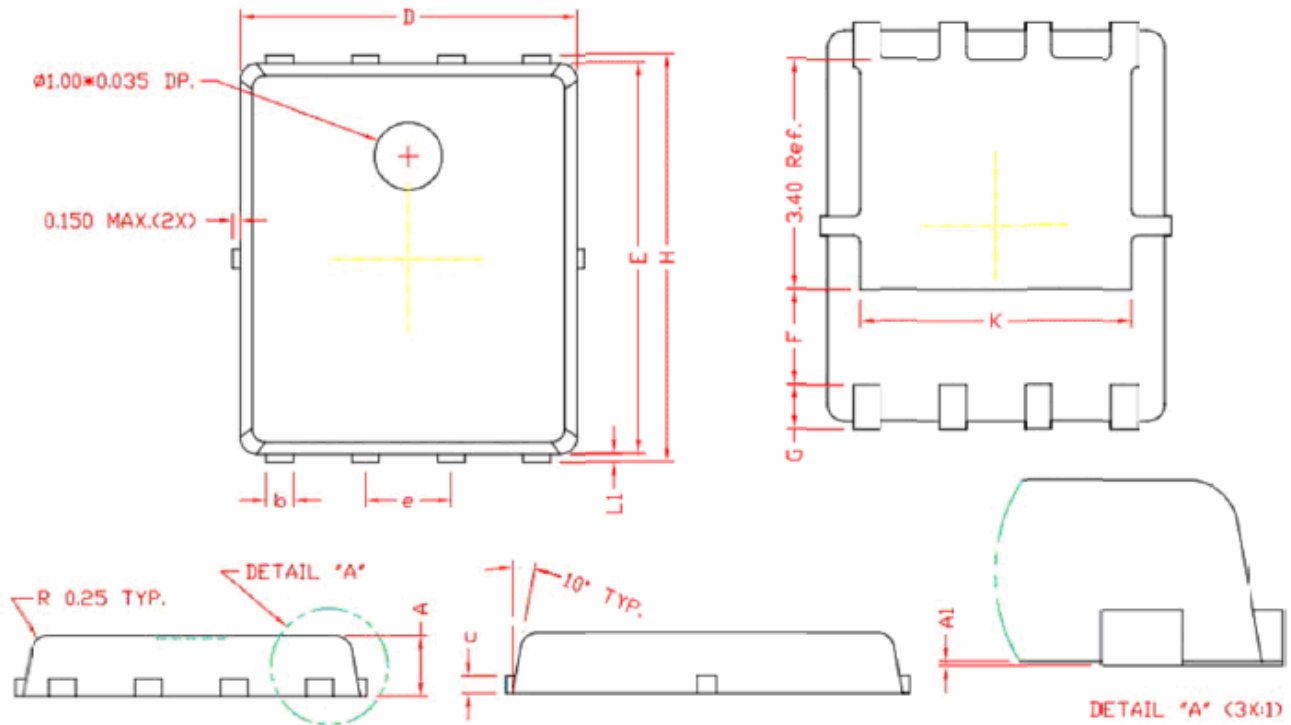


Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

| SYMBOL | MIN | NOM | MAX |
|--------|------------|------|------|
| A | 0.80 | 0.90 | 1.00 |
| A1 | 0.00 | 0.03 | 0.05 |
| b | 0.35 | 0.42 | 0.49 |
| c | 0.254 REF. | | |
| D | 4.90 | 5.00 | 5.10 |
| F | 1.40 REF. | | |
| E | 5.70 | 5.80 | 5.90 |
| e | 1.27 BSC. | | |
| H | 5.95 | 6.08 | 6.20 |
| L1 | 0.10 | 0.14 | 0.18 |
| G | 0.60 REF. | | |
| K | 4.00 REF. | | |

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