

## NCE P-Channel Super Trench Power MOSFET

<p><b>Description</b></p> <p>The NCEP40PT15G uses <b>Super Trench</b> technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of <math>R_{DS(ON)}</math> and <math>Q_g</math>. This device is ideal for high-frequency switching and synchronous rectification.</p> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● DC/DC Converter</li> <li>● Ideal for high-frequency switching and synchronous rectification</li> </ul>	<p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS} = -40V, I_D = -150A</math></li> <li>● <math>R_{DS(ON)} = 2.8m\Omega</math> (typical) @ <math>V_{GS} = -10V</math></li> <li>● <math>R_{DS(ON)} = 3.8m\Omega</math> (typical) @ <math>V_{GS} = -4.5V</math></li> <li>● Excellent gate charge x <math>R_{DS(on)}</math> product(FOM)</li> <li>● Very low on-resistance <math>R_{DS(on)}</math></li> <li>● 150 °C operating temperature</li> <li>● Pb-free lead plating</li> <li>● 100% UIS tested</li> </ul> <p style="text-align: center;"><b>100% UIS TESTED!</b> <b>100% ΔVs TESTED!</b></p>
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**DFN 5X6**

**Top View**

**Bottom View**

**Schematic Diagram**

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP40PT15G	NCEP40PT15G	DFN5X6-8L	-	-	-

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current-Continuous ( $T_C = 25^\circ C$ )	$I_D (T_C = 25^\circ C)$	-150	A
Drain Current-Continuous ( $T_C = 100^\circ C$ )	$I_D (T_C = 100^\circ C)$	-106	A
Drain Current-Continuous ( $T_A = 25^\circ C$ )	$I_D (T_A = 25^\circ C)$	-20.5	A
Pulsed Drain Current (Note 1)	$I_{DM}$	-600	A
Maximum Power Dissipation ( $T_C = 25^\circ C$ )	$P_D (T_C = 25^\circ C)$	150	W
Maximum Power Dissipation ( $T_A = 25^\circ C$ )	$P_D (T_A = 25^\circ C)$	2.5	W
Derating factor		1.2	W/ $^\circ C$
Single pulse avalanche energy (Note 5)	$E_{AS}$	1076	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case (Note 2)	$R_{\theta JC}$	0.83	$^\circ C/W$
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	50	$^\circ C/W$

## Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-40		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.0	-1.5	-2.2	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-75A	-	2.8	3.5	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-75A	-	3.9	6.0	mΩ
Gate resistance	R <sub>G</sub>	F=1.0MHz	-	5.5	-	Ω
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-75A	-	30	-	S
<b>Dynamic Characteristics</b> (Note 4)						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V, F=1.0MHz	-	8940	-	PF
Output Capacitance	C <sub>oss</sub>		-	1900	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	45	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-20V, I <sub>D</sub> =-75A V <sub>GS</sub> =-10V, R <sub>G</sub> =1.6Ω	-	18	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	13	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	90	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	15	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-20V, I <sub>D</sub> =-75A, V <sub>GS</sub> =-10V	-	104.4	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	20.8		nC
Gate-Drain Charge	Q <sub>gd</sub>		-	13.5		nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-75A	-		-1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	-150	A
Reverse Recovery Time	t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = -75A	-		35	nS
Reverse Recovery Charge	Q <sub>rr</sub>	di/dt = 100A/μs (Note 3)	-		85	nC

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. The value of R<sub>θJA</sub> is measured with the device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub>=25°C. The value in any given application depends on the user's specific board design.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition : T<sub>J</sub>=25°C, V<sub>DD</sub>=-20V, V<sub>G</sub>=-10V, L=0.5mH, R<sub>G</sub>=25Ω

Typical Electrical and Thermal Characteristics

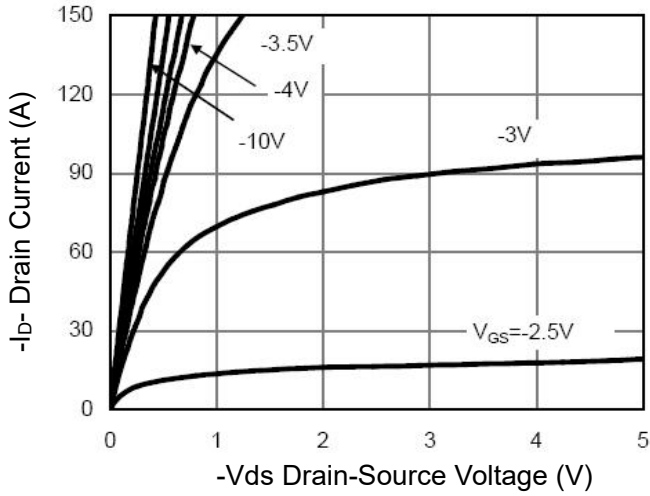


Figure 1 Output Characteristics

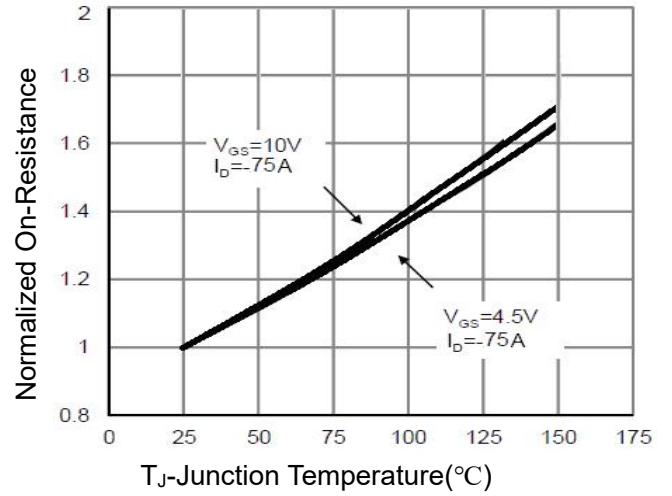


Figure 4 Rdson-Junction Temperature

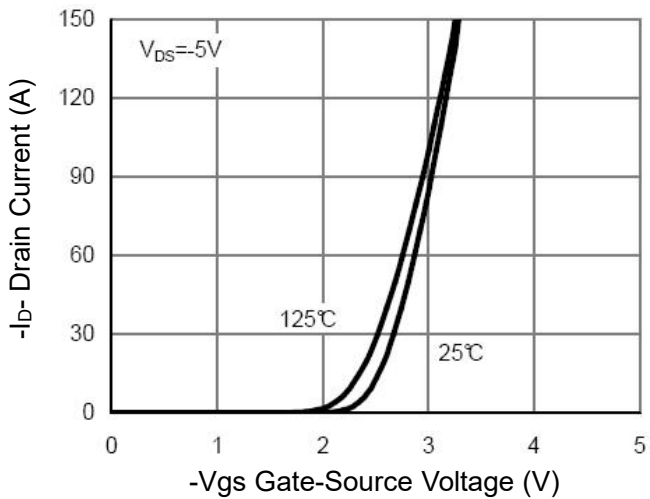


Figure 2 Transfer Characteristics

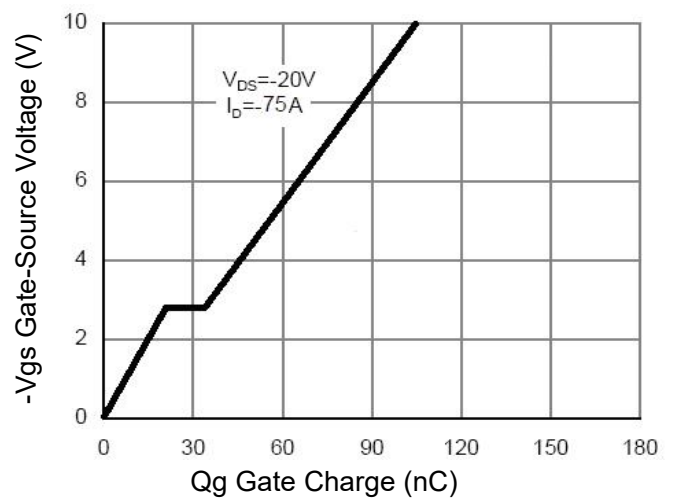


Figure 5 Gate Charge

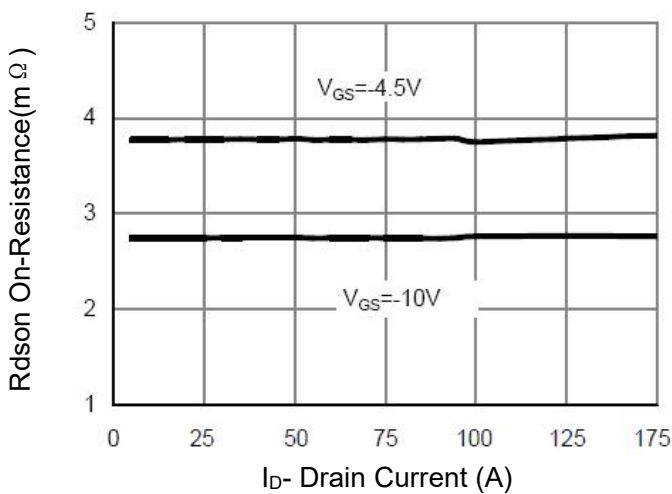


Figure 3 Rdson- Drain Current

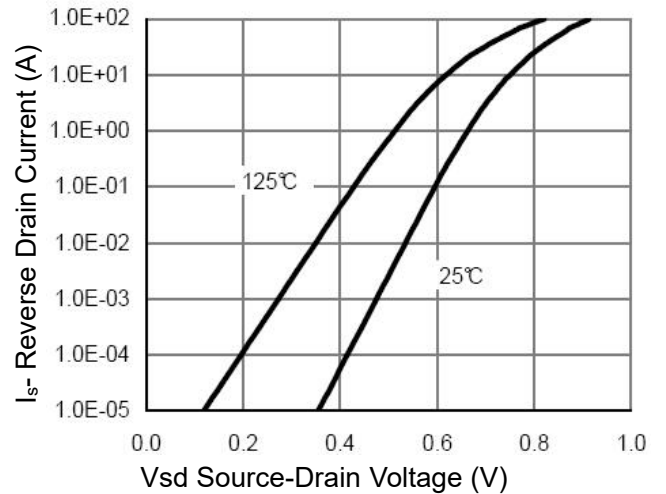


Figure 6 Source- Drain Diode Forward

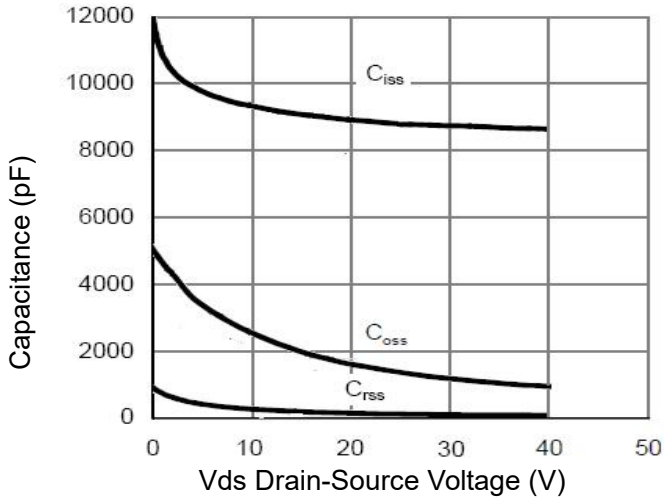


Figure 7 Capacitance vs Vds

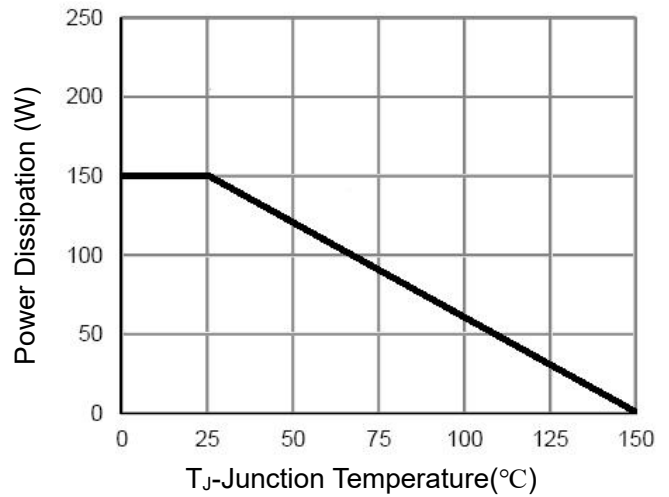


Figure 9 Power De-rating

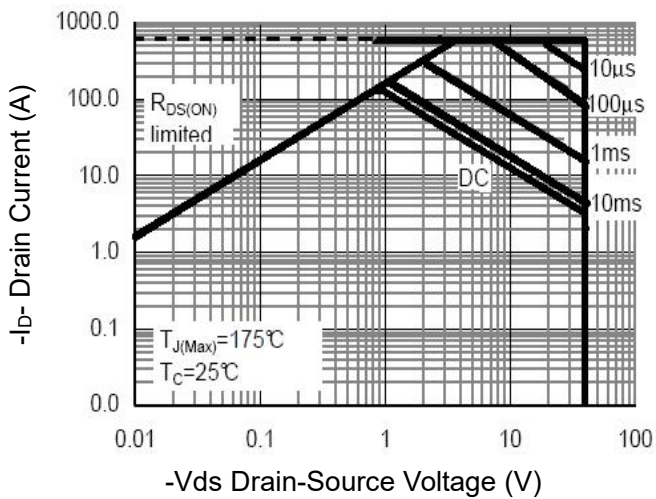


Figure 8 Safe Operation Area

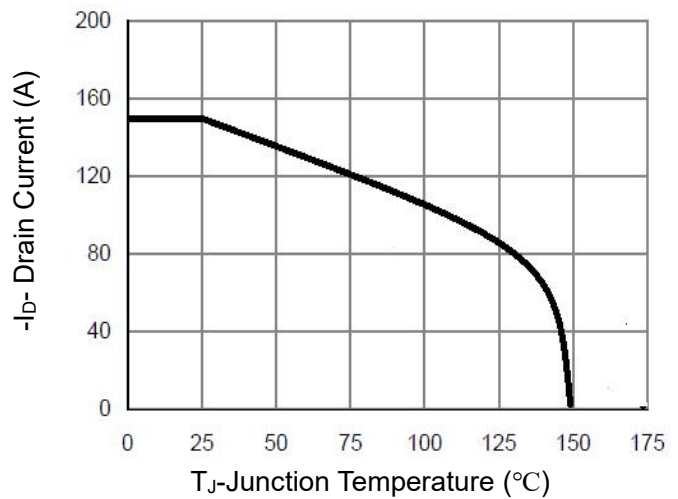


Figure 10 Current De-rating

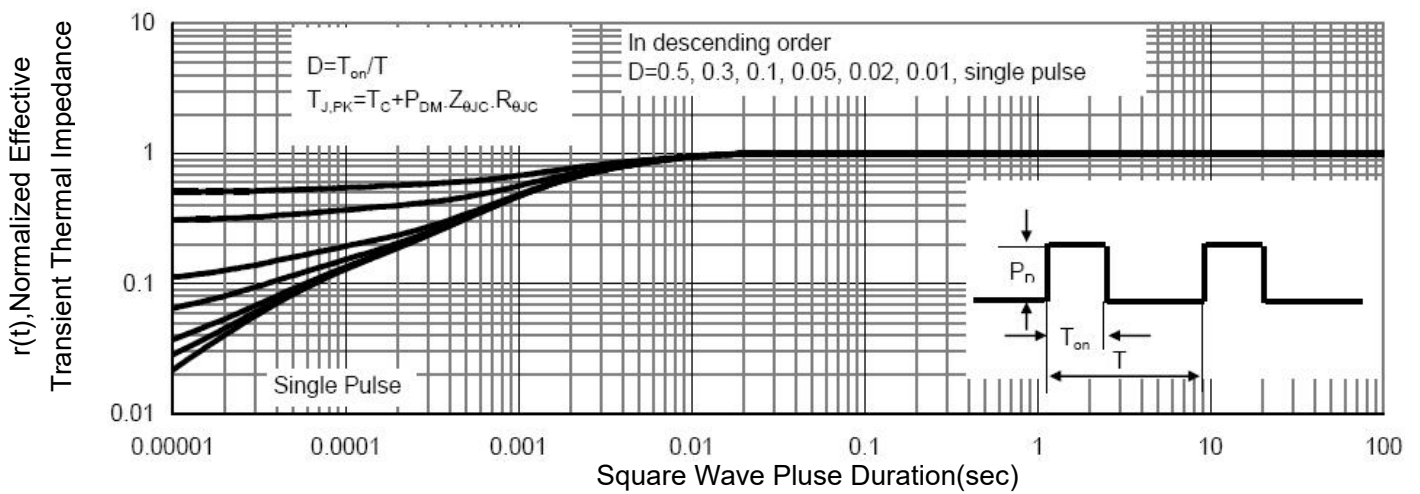
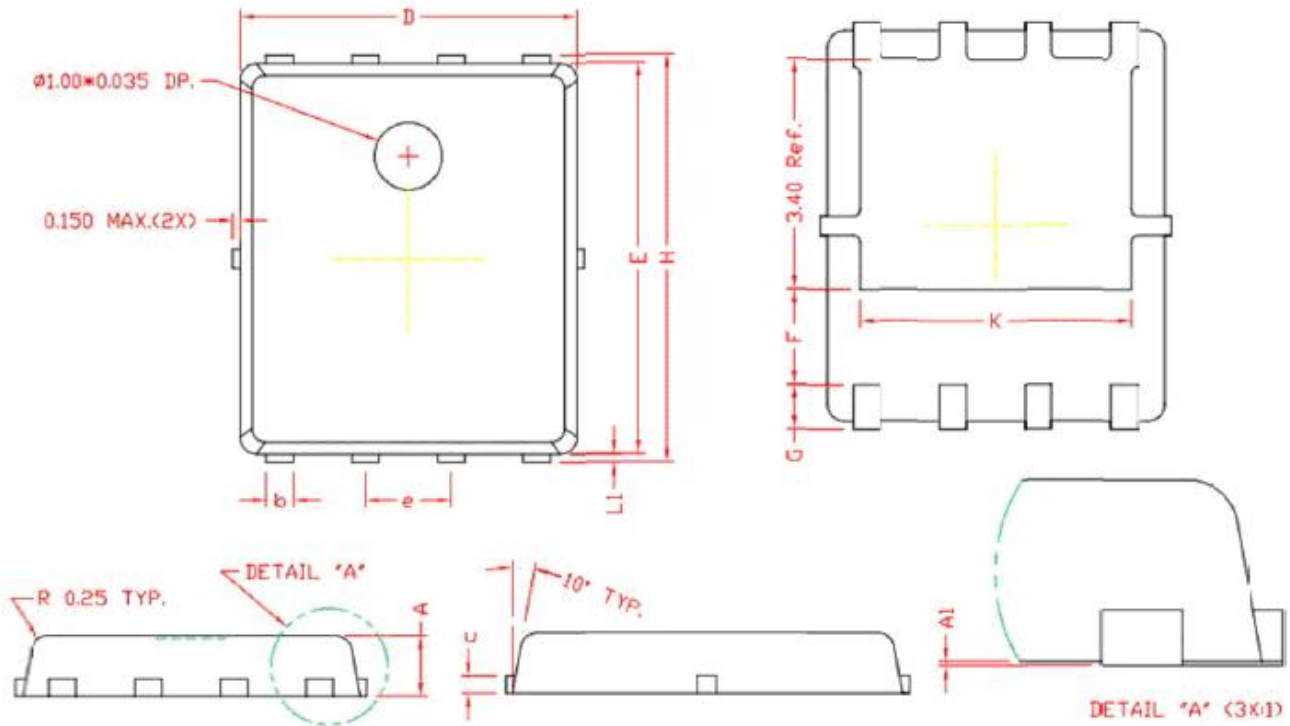


Figure 11 Normalized Maximum Transient Thermal Impedance

DFN5X6-8L Package Information



COMMON DIMENSIONS

(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NCM	MAX
A	0.80	0.90	1.00
A1	0.00	0.03	0.05
b	0.35	0.42	0.49
c	0.254 REF.		
D	4.90	5.00	5.10
F	1.40 REF.		
E	5.70	5.80	5.90
e	1.27 BSC.		
H	5.95	6.08	6.20
L1	0.10	0.14	0.18
G	0.60 REF.		
K	4.00 REF.		

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