



NCE N-Channel Super Trench Power MOSFET

Description

The NCEP40T13GU uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of $R_{DS(ON)}$ and Q_g . This device is ideal for high-frequency switching and synchronous rectification.

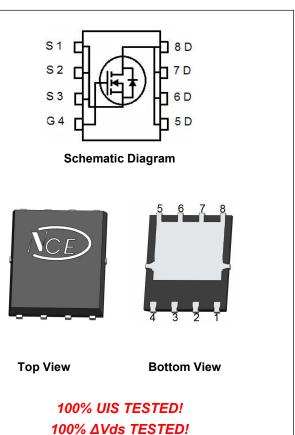
General Features

V_{DS} =40V,I_D =130A
 R_{DS(ON)}=1.8mΩ (typical) @ V_{GS}=10V
 R_{DS(ON)}=2.8mΩ (typical) @ V_{GS}=4.5V

- Excellent gate charge x R_{DS(on)} product(FOM)
- Very low on-resistance RDS(on)
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Package Marking and Ordering Information

V					
Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
P40T13GU	NCEP40T13GU	DFN5X6-8L	-	-	-

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	Vds	40	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	Ι _D	130	А
Drain Current-Continuous(T _c =100℃)	l _D (100℃)	100	A
Pulsed Drain Current	I _{DM}	520	A
Maximum Power Dissipation	PD	130	W
Derating factor		1.04	W/℃
Single pulse avalanche energy ^(Note 1)	E _{AS}	600	mJ
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C

Thermal Characteristic

Thermal Resistance, Junction-to-Case	Rejc	0.96	°C/W]
--------------------------------------	------	------	------	---



Electrical Characteristics (Tc=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Мах	Unit
Off Characteristics	I					
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250µA	40		-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =40V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics	I					
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =250µA	1.2	1.7	2.2	V
Desire Oscare On Otata Desiratores	_	V _{GS} =10V, I _D =65A	-	1.8	2.3	mΩ
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =65A	-	2.8	3.6	mΩ
Gate resistance	Rg	V _{DS} =0V,V _{GS} =0V,F=1.0MHz	-	2.5	-	Ω
Forward Transconductance	g fs	V _{DS} =5V,I _D =65A	-	62	-	S
Dynamic Characteristics	·					
Input Capacitance	Clss	V _{DS} =20V,V _{GS} =0V,	-	3334	-	PF
Output Capacitance	Coss		-	1025	-	PF
Reverse Transfer Capacitance	Crss	F=1.0MHz	-	57	-	PF
Switching Characteristics (Note 2)	·					
Turn-on Delay Time	t _{d(on)}		-	10	-	nS
Turn-on Rise Time	tr	V _{DD} =20V,I _D =65A V _{GS} =10V,R _G =1.6Ω	-	3	-	nS
Turn-Off Delay Time	t _{d(off)}		-	34	-	nS
Turn-Off Fall Time	t _f		-	3	-	nS
Total Gate Charge	Qg)/ 00)//L 05A	-	58.5	-	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=20V, I_{D}=65A,$	-	9.6	-	nC
Gate-Drain Charge	Q _{gd}	V _{GS} =10V	-	10	-	nC
Drain-Source Diode Characteristics						
Diode Forward Voltage	V _{SD}	V _{GS} =0V,I _S =65A	-		1.2	V
Diode Forward Current	Is		-	-	130	Α
Reverse Recovery Time	t _{rr}	T _J = 25°C, I _F = I _S	-		23	nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs	-		67	nC

Notes:

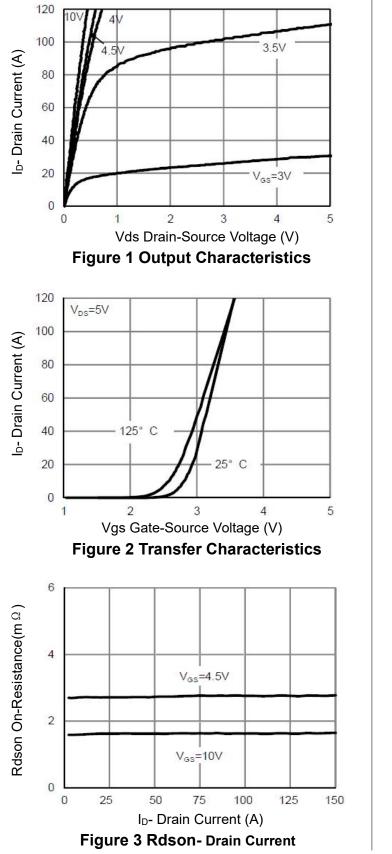
1. EAS condition : Tj=25 $^\circ C$,V_DD=20V,V_G=10V,L=0.5mH,Rg=25 Ω

2. Guaranteed by design, not subject to production

3. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heats in k, assuming a maximum junction temperature of $TJ(MAX)=150^{\circ}$ C. The SOA curve provides a single pulse rating.



Typical Electrical and Thermal Characteristics



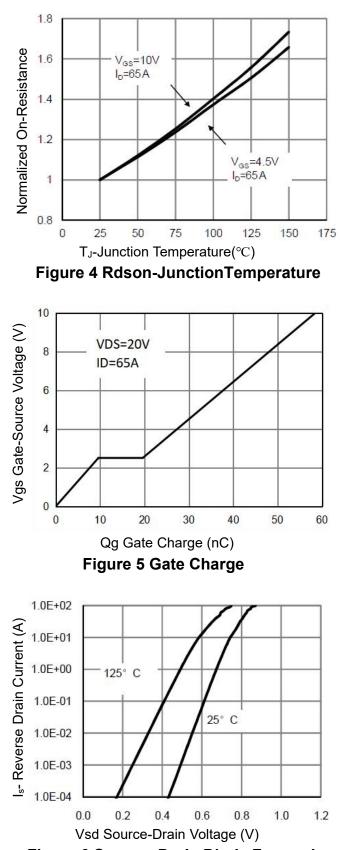


Figure 6 Source- Drain Diode Forward



http://www.ncepower.com

NCEP40T13GU

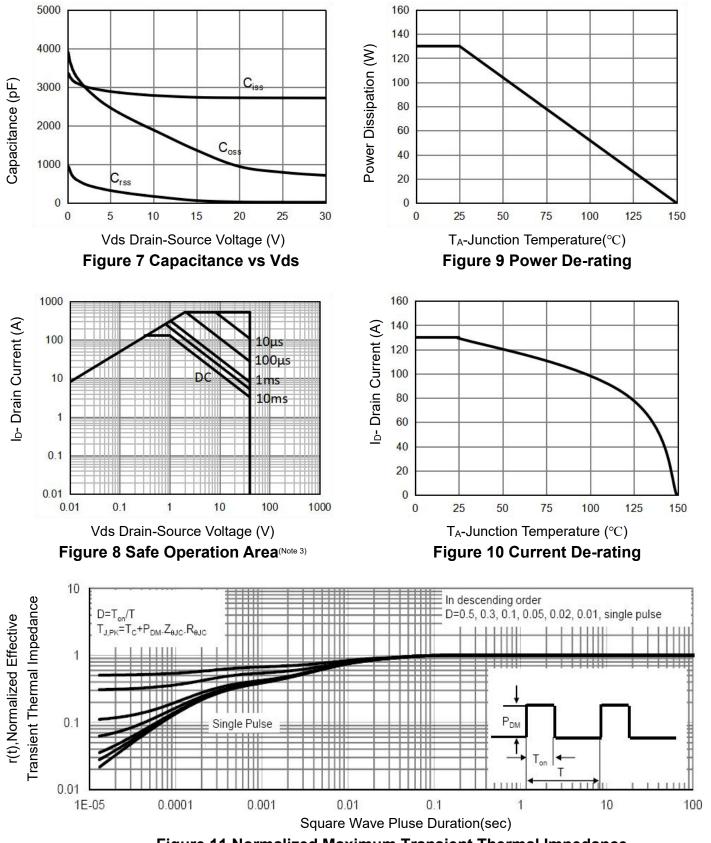
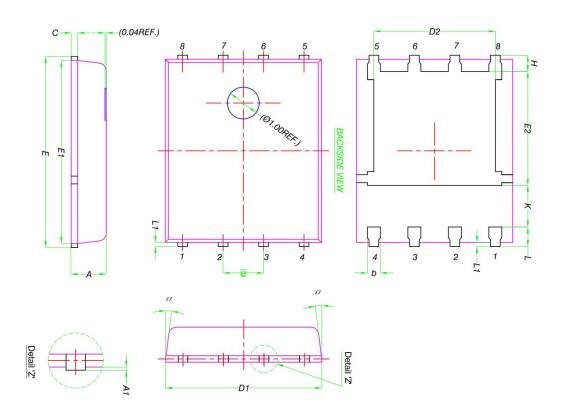


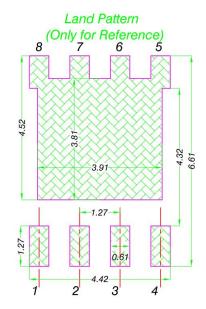
Figure 11 Normalized Maximum Transient Thermal Impedance



DFN5X6-8L Package Information



044	MILLIMETERS			
DIM.	MIN.	NOM.	MAX.	
А	0.90	1.00	1.10	
A1	0	-	0.05	
b	0.33	0.41	0.51	
С	0.20	0.25	0.30	
D1	4.80	4.90	5.00	
D2	3.61	3.81	3.96	
Ε	5.90	6.00	6.10	
E1	5.70	5.75	5.80	
E2	3.38	3.58	3.78	
е	1.27 BSC			
Н	0.41	0.51	0.61	
К	1.10	-	-	
L	0.51	0.61	0.71	
L1	0.06	0.13	0.20	
α	0°	-	12°	



Note:

- All Dimension Are In mm.
 All Dimension Are In mm.
 Package Body Sizes Exclude Mold Flash, Protrusion Or Gate Burrs. Mold Flash, Protrusion Or Gate Burrs Shall Not Exceed 0.10 mm Per Side.
 Package Body Sizes Determined At The Outermost Extremes Of The Plastic Body Exclusive Of Mold Flash, Tie Bar, Tie Bar Burrs, Gate Burrs And Interlead Flash, But Lecluding Any Mismatch Patruage Tha Ton And Pottorn Of The Plastic Body. But Including Any Mismatch Between The Top And Bottom Of The Plastic Body.
- 4. The Package Top May Be Smaller Than The Package Bottom.



Attention:

- Any and all NCE power products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your NCE power representative nearest you before using any NCE power products described or contained herein in such applications.
- NCE power assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all NCE power products described or contained herein.
- Specifications of any and all NCE power products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.
- NCE power Semiconductor CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.
- In the event that any or all NCE power products(including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.
- No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of NCE power Semiconductor CO.,LTD.
- Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. NCE power believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.
- Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the NCE power product that you intend to use.
- This catalog provides information as of Sep.2010. Specifications and information herein are subject to change without notice.